

**GURU GOBIND SINGH INDRAPRASTHA UNIVERSITY (GGSIPU)**  
**CET Held on 31/05/2023, Roll Number Wise Common Merit List (CML) of All Present Candidates.**  
**Programme : GGSIPU M.Tech (ECE) VLSI Design (Reg.) CET 2023, CET Code: 140**

S.No	Name of Program	Roll Number	Application Number	Applicant Name	CML
1	GGSIPU M.Tech. CET (Electronics & Communication Engineering) (Regular) and (VLSI Design) (Regular) 2023 [CET	2314000001	140230000003	ADITYA BALODA	7
2	GGSIPU M.Tech. CET (Electronics & Communication Engineering) (Regular) and (VLSI Design) (Regular) 2023 [CET	2314000002	140230000007	SHIVANI VASHISTH	10
3	GGSIPU M.Tech. CET (Electronics & Communication Engineering) (Regular) and (VLSI Design) (Regular) 2023 [CET	2314000003	140230000009	PIYUSH	3
4	GGSIPU M.Tech. CET (Electronics & Communication Engineering) (Regular) and (VLSI Design) (Regular) 2023 [CET	2314000004	140230000012	SAGAR SHARMA	1
5	GGSIPU M.Tech. CET (Electronics & Communication Engineering) (Regular) and (VLSI Design) (Regular) 2023 [CET	2314000007	140230000020	JATIN SINGH	6
6	GGSIPU M.Tech. CET (Electronics & Communication Engineering) (Regular) and (VLSI Design) (Regular) 2023 [CET	2314000008	140230000023	DAKSH SHARMA	5
7	GGSIPU M.Tech. CET (Electronics & Communication Engineering) (Regular) and (VLSI Design) (Regular) 2023 [CET	2314000010	140230000035	SHUBHAM SAXENA	4
8	GGSIPU M.Tech. CET (Electronics & Communication Engineering) (Regular) and (VLSI Design) (Regular) 2023 [CET	2314000011	140230000038	E RICHARD PRAJWAL DAS	2
9	GGSIPU M.Tech. CET (Electronics & Communication Engineering) (Regular) and (VLSI Design) (Regular) 2023 [CET	2314000012	140230000041	ALNOOR PARVEEN	9
10	GGSIPU M.Tech. CET (Electronics & Communication Engineering) (Regular) and (VLSI Design) (Regular) 2023 [CET	2314000013	140230000043	ADITYA GOLA	8

Note : The status Region, Category or Sub-category as mentioned in the Application form or as rectified by the Applicant will be used for counselling, subject to verification of relevant documents