



Guru Gobind Singh Indraprastha University

Dwarka Sector 16-C, New Delhi – 110078

Examination Division (Ph.D. Branch)

E-mail: phdeval@ipu.ac.in


F. No.: GGSIPU/Exam/Ph.D./Notification/2022/4268

Dated: 12th July, 2022

Ph.D. NOTIFICATION


Guru Gobind Singh Indraprastha University is pleased to notify the acceptance of the Ph.D. thesis of following candidate and declare them eligible for the award of Doctor of Philosophy (Ph.D.). The relevant details relating to the award of the said Ph.D. degree is indicated below against the name of the candidate:

S. No.	Name of the candidate	Enrolment No.	Name of the School	Subject	Title of the thesis	Name of the Supervisor/ Co-supervisor / Joint Supervisor	Effective Date of Award
1.	Mr. Vivek Jangra	05116493316	USICT	Electronic and Communication Engineering.	Design Optimization of CMOS Oscillator Circuits for Low Power Applications	Prof. (Dr.) Manoj Kumar	08.07.2022


(Dr. Gulshan Kumar)
In-Charge (Ph.D.)

Copy to:

1. Dean, University School of Studies, Guru Gobind Singh Indraprastha University
2. Director (Research and Consultancy), Guru Gobind Singh Indraprastha University
3. Librarian, Guru Gobind Singh Indraprastha University
4. Supervisors of the Research Scholar
5. In-charge, UITS, GGSIPU with request to publish the said notification on the University Website
6. In-Charge, Server Room & EDP, Examination Division, GGSIPU
7. AR to VC Secretariat for kind information of Hon'ble Vice Chancellor, GGSIPU
8. AR to Registrar office for information of Registrar, GGSIPU
9. PS to COE, Guru Gobind Singh Indraprastha University
10. The concerned Research Scholars
11. The main file of Research Scholars


(Jasjeet Kaur)
Section Officer (Ph.D.)