SCHEME OF EXAMINATION

and

SYLLABI

for

Bachelor of Technology
Instrumentation and Control Engineering

Offered by
University School of Engineering and Technology

1st SEMESTER TO 8th SEMESTER

Guru Gobind Singh Indraprastha University
Dwarka, Delhi – 110078 [INDIA]
www.ipu.ac.in
## BACHELOR OF TECHNOLOGY
(COMMON TO ALL BRANCHES)
FIRST SEMESTER EXAMINATION

<table>
<thead>
<tr>
<th>Code No.</th>
<th>Paper ID</th>
<th>Paper</th>
<th>L</th>
<th>T/P</th>
<th>Credits</th>
<th>Status</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td><strong>THEORY PAPERS</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>ETMA-101</td>
<td></td>
<td>Applied Mathematics-I</td>
<td>3</td>
<td>1</td>
<td>4</td>
<td>M</td>
</tr>
<tr>
<td>ETPH-103</td>
<td></td>
<td>Applied Physics-I</td>
<td>2</td>
<td>1</td>
<td>3</td>
<td>M</td>
</tr>
<tr>
<td>ETME-105</td>
<td></td>
<td>Manufacturing Processes</td>
<td>3</td>
<td>0</td>
<td>3</td>
<td>M</td>
</tr>
<tr>
<td>ETEE-107</td>
<td></td>
<td>Electrical Technology</td>
<td>3</td>
<td>0</td>
<td>3</td>
<td>M</td>
</tr>
<tr>
<td>ETHS-109</td>
<td></td>
<td>Human Values and Professional Ethics-I#</td>
<td>1</td>
<td>1</td>
<td>1</td>
<td>--</td>
</tr>
<tr>
<td>ETCS-111</td>
<td></td>
<td>Fundamentals of Computing</td>
<td>2</td>
<td>0</td>
<td>2</td>
<td>--</td>
</tr>
<tr>
<td>ETCH-113</td>
<td></td>
<td>Applied Chemistry</td>
<td>2</td>
<td>1</td>
<td>3</td>
<td>M</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td><strong>PRACTICAL/VIVA VOCE</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>ETPH-151</td>
<td></td>
<td>Applied Physics Lab-I</td>
<td>-----</td>
<td>2</td>
<td>1</td>
<td>----</td>
</tr>
<tr>
<td>ETEE-153</td>
<td></td>
<td>Electrical Technology Lab</td>
<td>-----</td>
<td>2</td>
<td>1</td>
<td>M</td>
</tr>
<tr>
<td>ETME-155</td>
<td></td>
<td>Workshop Practice</td>
<td>-----</td>
<td>3</td>
<td>2</td>
<td>M</td>
</tr>
<tr>
<td>ETME-157</td>
<td></td>
<td>Engineering Graphics Lab</td>
<td>-----</td>
<td>3</td>
<td>2</td>
<td>----</td>
</tr>
<tr>
<td>ETCS-157</td>
<td></td>
<td>Fundamentals of Computing Lab</td>
<td>-----</td>
<td>2</td>
<td>1</td>
<td>--</td>
</tr>
<tr>
<td>ETCH-161</td>
<td></td>
<td>Applied Chemistry Lab</td>
<td>-----</td>
<td>2</td>
<td>1</td>
<td>--</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td><strong>TOTAL</strong></td>
<td></td>
<td></td>
<td>16</td>
<td>18</td>
<td>27</td>
<td></td>
</tr>
</tbody>
</table>

M: Mandatory for award of degree

NUES (Non University Examination System)

*NCC/NSS can be completed in any semester from Semester 1 – Semester 4. It will be evaluated internally by the respective institute. The credit for this will be given after fourth Semester for the students enrolled from the session 2014-15 onwards. The camps/classes will be held either during Weekends/Holidays or Winter/Summer Vacations.*

Scheme and Syllabi for B. Tech-ICE, 1st year (Common to all branches) w.e.f batch 2014-15 and (2nd, 3rd & 4th years) w.e.f batch 2013-14 approved in the 22nd BOS of USET on 30th June, 2014 and approved in the 37th AC Sub Committee Meeting held on 10th July, 2014.
### BACHELOR OF TECHNOLOGY
#### (COMMON TO ALL BRANCHES)
#### SECOND SEMESTER EXAMINATION

<table>
<thead>
<tr>
<th>Code No.</th>
<th>Paper ID</th>
<th>Paper</th>
<th>L</th>
<th>T/P</th>
<th>Credits</th>
<th>Status</th>
</tr>
</thead>
<tbody>
<tr>
<td>ETMA-102</td>
<td></td>
<td>Applied Mathematics-II</td>
<td>3</td>
<td>1</td>
<td>4</td>
<td>M</td>
</tr>
<tr>
<td>ETPH-104</td>
<td></td>
<td>Applied Physics-II</td>
<td>2</td>
<td>1</td>
<td>3</td>
<td></td>
</tr>
<tr>
<td>ETEC-106</td>
<td></td>
<td>Electronic Devices</td>
<td>3</td>
<td>0</td>
<td>3</td>
<td>M</td>
</tr>
<tr>
<td>ETCS-108</td>
<td></td>
<td>Introduction to Programming</td>
<td>3</td>
<td>0</td>
<td>3</td>
<td>M</td>
</tr>
<tr>
<td>ETME-110</td>
<td></td>
<td>Engineering Mechanics</td>
<td>2</td>
<td>1</td>
<td>3</td>
<td>--</td>
</tr>
<tr>
<td>ETHS-112</td>
<td></td>
<td>Communication Skills</td>
<td>2</td>
<td>1</td>
<td>3</td>
<td>--</td>
</tr>
<tr>
<td>ETEN-114</td>
<td></td>
<td>Environmental Studies</td>
<td>2</td>
<td>1</td>
<td>3</td>
<td>--</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Code No.</th>
<th>Paper ID</th>
<th>Paper</th>
<th>L</th>
<th>T/P</th>
<th>Credits</th>
<th>Status</th>
</tr>
</thead>
<tbody>
<tr>
<td>ETPH-152</td>
<td></td>
<td>Applied Physics Lab-II</td>
<td></td>
<td>2</td>
<td>1</td>
<td></td>
</tr>
<tr>
<td>ETCS-154</td>
<td></td>
<td>Programming Lab</td>
<td></td>
<td>2</td>
<td>1</td>
<td>M</td>
</tr>
<tr>
<td>ETEC-156</td>
<td></td>
<td>Electronic Devices Lab</td>
<td></td>
<td>2</td>
<td>1</td>
<td>M</td>
</tr>
<tr>
<td>ETME-158</td>
<td></td>
<td>Engineering Mechanics Lab</td>
<td></td>
<td>2</td>
<td>1</td>
<td>--</td>
</tr>
<tr>
<td>ETEN-160</td>
<td></td>
<td>Environmental Studies Lab</td>
<td></td>
<td>2</td>
<td>1</td>
<td>--</td>
</tr>
<tr>
<td>NCC/NSS*#</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>--</td>
</tr>
</tbody>
</table>

**TOTAL**  
17 15 27

M: Mandatory for award of degree  
#NUES (Non University Examination System)  
*#NCC/NSS can be completed in any semester from Semester 1 – Semester 4. It will be evaluated internally by the respective institute. The credit for this will be given after fourth Semester for the students enrolled from the session 2014-15 onwards. The camps/classes will be held either during Weekends/Holidays or Winter/Summer Vacations.  

---

Scheme and Syllabi for B. Tech-ICE, 1st year (Common to all branches) **w.e.f batch 2014-15** and (2nd, 3rd & 4th years) **w.e.f batch 2013-14** approved in the 22nd BOS of USET on 30th June, 2014 and approved in the 37th AC Sub Committee Meeting held on 10th July, 2014.
### BACHELOR OF TECHNOLOGY
**INSTRUMENTATION AND CONTROL ENGINEERING**

**THIRD SEMESTER EXAMINATION**

<table>
<thead>
<tr>
<th>Code No.</th>
<th>Paper ID</th>
<th>Paper</th>
<th>L</th>
<th>T/P</th>
<th>Credits</th>
<th>Status</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td><strong>THEORY PAPERS</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>ETMA 201</td>
<td></td>
<td>Applied Mathematics – III</td>
<td>3</td>
<td>1</td>
<td>4</td>
<td></td>
</tr>
<tr>
<td>ETIC 203</td>
<td></td>
<td>Sensors and Transducers</td>
<td>3</td>
<td>1</td>
<td>4</td>
<td>M</td>
</tr>
<tr>
<td>ETEC 205</td>
<td></td>
<td>Switching Theory and Logic Design</td>
<td>3</td>
<td>1</td>
<td>4</td>
<td>M</td>
</tr>
<tr>
<td>ETEE 207</td>
<td></td>
<td>Circuits and Systems</td>
<td>3</td>
<td>1</td>
<td>4</td>
<td></td>
</tr>
<tr>
<td>ETCS 209</td>
<td></td>
<td>Data Structures</td>
<td>3</td>
<td>1</td>
<td>4</td>
<td></td>
</tr>
<tr>
<td>ETIC 211</td>
<td></td>
<td>Basics of Measurements</td>
<td>3</td>
<td>1</td>
<td>4</td>
<td>M</td>
</tr>
<tr>
<td></td>
<td></td>
<td><strong>PRACTICAL/VIVA VOCE</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>ETIC 251</td>
<td></td>
<td>Sensors and Transducers Lab</td>
<td>0</td>
<td>2</td>
<td>1</td>
<td>M</td>
</tr>
<tr>
<td>ETEC 253</td>
<td></td>
<td>Switching Theory and Logic Design Lab</td>
<td>0</td>
<td>2</td>
<td>1</td>
<td>M</td>
</tr>
<tr>
<td>ETCS 255</td>
<td></td>
<td>Data Structures Lab.</td>
<td>0</td>
<td>2</td>
<td>1</td>
<td></td>
</tr>
<tr>
<td>ETEE 257</td>
<td></td>
<td>Circuit and Systems Lab*</td>
<td>0</td>
<td>2</td>
<td>1</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>NCC/NSS**##</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td><strong>TOTAL</strong></td>
<td>18</td>
<td>14</td>
<td>28</td>
<td></td>
</tr>
</tbody>
</table>

M: Mandatory for award of degree  
#NUES (Non University Examination System)  
**NCC/NSS can be completed in any semester from Semester 1 – Semester 4. It will be evaluated internally by the respective institute. The credit for this will be given after fourth Semester for the students enrolled from the session 2014-15 onwards.**  
* A few lab experiments must be performed using any circuit simulation software e.g. PSPICE/MATLAB/ETAP/Scilab/LabVIEW

---

Scheme and Syllabi for B. Tech-ICE, 1st year (Common to all branches) w.e.f batch 2014-15 and (2nd, 3rd & 4th years) w.e.f batch 2013-14 approved in the 22nd BOS of USET on 30th June, 2014 and approved in the 37th AC Sub Committee Meeting held on 10th July, 2014.
BACHELOR OF TECHNOLOGY  
(INSTRUMENTATION AND CONTROL ENGINEERING) 
FOURTH SEMESTER EXAMINATION

<table>
<thead>
<tr>
<th>Code No.</th>
<th>Paper ID</th>
<th>Paper</th>
<th>L</th>
<th>T/P</th>
<th>Credits</th>
<th>Status</th>
</tr>
</thead>
<tbody>
<tr>
<td>ETIC-204</td>
<td></td>
<td>Measurements and Instrumentation</td>
<td>3</td>
<td>0</td>
<td>3</td>
<td>M</td>
</tr>
<tr>
<td>ETEE-212</td>
<td></td>
<td>Control Systems</td>
<td>3</td>
<td>1</td>
<td>4</td>
<td>M</td>
</tr>
<tr>
<td>ETIC-206</td>
<td></td>
<td>Power Electronics</td>
<td>3</td>
<td>1</td>
<td>4</td>
<td></td>
</tr>
<tr>
<td>ETIC-208</td>
<td></td>
<td>Theory and Applications of Integrated Circuits</td>
<td>3</td>
<td>1</td>
<td>4</td>
<td>M</td>
</tr>
<tr>
<td>ETIC-210</td>
<td></td>
<td>Electrical Machines</td>
<td>3</td>
<td>1</td>
<td>4</td>
<td></td>
</tr>
<tr>
<td>ETIC-212</td>
<td></td>
<td>Communication Systems</td>
<td>3</td>
<td>1</td>
<td>4</td>
<td></td>
</tr>
<tr>
<td>ETIC-252</td>
<td></td>
<td>Measurements and Instrumentation Lab</td>
<td>0</td>
<td>2</td>
<td>1</td>
<td>M</td>
</tr>
<tr>
<td>ETEE-260</td>
<td></td>
<td>Control Systems Lab</td>
<td>0</td>
<td>2</td>
<td>1</td>
<td>M</td>
</tr>
<tr>
<td>ETIC-256</td>
<td></td>
<td>Power Electronics Lab</td>
<td>0</td>
<td>2</td>
<td>1</td>
<td></td>
</tr>
<tr>
<td>ETIC-258</td>
<td></td>
<td>Theory and Applications of Integrated Circuits Lab</td>
<td>0</td>
<td>2</td>
<td>1</td>
<td>M</td>
</tr>
<tr>
<td>ETIC-254</td>
<td></td>
<td>Electrical Machines Lab</td>
<td>0</td>
<td>2</td>
<td>1</td>
<td></td>
</tr>
<tr>
<td>ETSS-250</td>
<td></td>
<td>NSS/NCC*</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td></td>
</tr>
<tr>
<td>TOTAL</td>
<td></td>
<td></td>
<td>18</td>
<td>15</td>
<td>29</td>
<td></td>
</tr>
</tbody>
</table>

M: Mandatory for award of degree  
*NCC/NSS can be completed in any semester from Semester 1 – Semester 4. It will be evaluated internally by the respective institute. The credit for this will be given after fourth Semester for the students enrolled from the session 2014-15 onwards.  
NOTE: 4 weeks Industrial / In-house Instrumentation Workshop (Lab needs to be developed) will be held after fourth semester. However, Viva-Voce will be conducted in the fifth semester (ETIC 359).
### BACHELOR OF TECHNOLOGY
**(INSTRUMENTATION AND CONTROL ENGINEERING)**

#### FIFTH SEMESTER EXAMINATION

<table>
<thead>
<tr>
<th>Code No.</th>
<th>Paper ID</th>
<th>Paper</th>
<th>L</th>
<th>T/P</th>
<th>Credits</th>
<th>Status</th>
</tr>
</thead>
<tbody>
<tr>
<td>ETHS 301</td>
<td></td>
<td>Communication Skills for Professionals</td>
<td>2</td>
<td>0</td>
<td>1</td>
<td></td>
</tr>
<tr>
<td>ETIC 303</td>
<td></td>
<td>Industrial Instrumentation</td>
<td>3</td>
<td>1</td>
<td>4</td>
<td>M</td>
</tr>
<tr>
<td>ETEC 305</td>
<td></td>
<td>Microprocessors and Microcontrollers</td>
<td>3</td>
<td>1</td>
<td>4</td>
<td>M</td>
</tr>
<tr>
<td>ETEC 309</td>
<td></td>
<td>Digital System Design</td>
<td>3</td>
<td>1</td>
<td>4</td>
<td></td>
</tr>
<tr>
<td>ETIC 309</td>
<td></td>
<td>Object Oriented Programming using JAVA</td>
<td>3</td>
<td>0</td>
<td>3</td>
<td></td>
</tr>
<tr>
<td>ETMS 311</td>
<td></td>
<td>Industrial Management</td>
<td>3</td>
<td>0</td>
<td>3</td>
<td></td>
</tr>
</tbody>
</table>

#### PRACTICAL/VIVA VOCE

<table>
<thead>
<tr>
<th>Code No.</th>
<th>Paper ID</th>
<th>Paper</th>
<th>L</th>
<th>T/P</th>
<th>Credits</th>
<th>Status</th>
</tr>
</thead>
<tbody>
<tr>
<td>ETHS-351</td>
<td></td>
<td>Communication Skills for Professionals Lab</td>
<td>0</td>
<td>2</td>
<td>1</td>
<td></td>
</tr>
<tr>
<td>ETIC-353</td>
<td></td>
<td>Industrial Instrumentation Lab</td>
<td>0</td>
<td>2</td>
<td>1</td>
<td>M</td>
</tr>
<tr>
<td>ETEC-355</td>
<td></td>
<td>Microprocessors and Microcontrollers Lab</td>
<td>0</td>
<td>2</td>
<td>1</td>
<td></td>
</tr>
<tr>
<td>ETEC-351</td>
<td></td>
<td>Digital System Design Lab</td>
<td>0</td>
<td>2</td>
<td>1</td>
<td></td>
</tr>
<tr>
<td>ETIC-357</td>
<td></td>
<td>Object Oriented Programming using JAVA Lab</td>
<td>0</td>
<td>2</td>
<td>1</td>
<td></td>
</tr>
<tr>
<td>ETIC-359</td>
<td></td>
<td>Industrial training / In-house Instrumentation Workshop#</td>
<td>0</td>
<td>0</td>
<td>1</td>
<td></td>
</tr>
</tbody>
</table>

**TOTAL** 17 13 25

# NUES

M: Mandatory for the award of degree.

*Viva-Voce for evaluation of Industrial Training / In-house Workshop will be conducted in this semester.*
# BACHELOR OF TECHNOLOGY
## (INSTRUMENTATION AND CONTROL ENGINEERING)
### SIXTH SEMESTER EXAMINATION

<table>
<thead>
<tr>
<th>Code No.</th>
<th>Paper ID</th>
<th>Paper</th>
<th>L</th>
<th>T/P</th>
<th>Credits</th>
<th>Status</th>
</tr>
</thead>
<tbody>
<tr>
<td>ETIC-302</td>
<td></td>
<td>Pneumatic and Hydraulic Instrumentation</td>
<td>3</td>
<td>1</td>
<td>4</td>
<td>M</td>
</tr>
<tr>
<td>ETIC-304</td>
<td></td>
<td>Process Control</td>
<td>3</td>
<td>1</td>
<td>4</td>
<td>M</td>
</tr>
<tr>
<td>ETEC-306</td>
<td></td>
<td>Digital Signal Processing</td>
<td>3</td>
<td>1</td>
<td>4</td>
<td></td>
</tr>
<tr>
<td>ETIC-308</td>
<td></td>
<td>Analytical Instrumentation</td>
<td>3</td>
<td>1</td>
<td>4</td>
<td></td>
</tr>
<tr>
<td>ETIC-310</td>
<td></td>
<td>Modern Control Systems</td>
<td>3</td>
<td>1</td>
<td>4</td>
<td>M</td>
</tr>
<tr>
<td>ETEC-310</td>
<td></td>
<td>Data Communication and Networks</td>
<td>3</td>
<td>1</td>
<td>4</td>
<td></td>
</tr>
</tbody>
</table>

## PRACTICAL/VIVA VOCE

<table>
<thead>
<tr>
<th>Code No.</th>
<th>Paper ID</th>
<th>Paper</th>
<th>L</th>
<th>T/P</th>
<th>Credits</th>
<th>Status</th>
</tr>
</thead>
<tbody>
<tr>
<td>ETIC-352</td>
<td></td>
<td>Pneumatic and Hydraulic Instrumentation Lab</td>
<td>0</td>
<td>2</td>
<td>1</td>
<td>M</td>
</tr>
<tr>
<td>ETIC-354</td>
<td></td>
<td>Process Control Lab</td>
<td>0</td>
<td>2</td>
<td>1</td>
<td>M</td>
</tr>
<tr>
<td>ETEC-356</td>
<td></td>
<td>*Digital Signal Processing Lab.</td>
<td>0</td>
<td>2</td>
<td>1</td>
<td></td>
</tr>
<tr>
<td>ETIC-358</td>
<td></td>
<td>Modern Control Systems Lab</td>
<td>0</td>
<td>2</td>
<td>1</td>
<td>M</td>
</tr>
<tr>
<td>ETEC-358</td>
<td></td>
<td>Data Communication and Networks Lab</td>
<td>0</td>
<td>2</td>
<td>1</td>
<td></td>
</tr>
</tbody>
</table>

**TOTAL** 18 16 29

M: Mandatory for award of degree
#NUES (Non University Examination System)

**Note:** Minimum of 4-6 weeks of industrial training related to ICE will be held after 6th semester; however, viva-voce will be conducted in 7th Semester (ETIC 459).

**Imp:** Elective Paper will be floated in 7th Semester, if one-third of the total students opt for the same. It is advised that the decision about the elective subject for 7th Semester is done before the 15th April every year before end of 6th semester.

*A few lab experiments must be performed using any circuit simulation software e.g. PSPICE/MATLAB/ETAP/Scilab/LabVIEW*
## BACHELOR OF TECHNOLOGY

**INSTRUMENTATION AND CONTROL ENGINEERING**

### SEVENTH SEMESTER EXAMINATION

<table>
<thead>
<tr>
<th>Code No.</th>
<th>Paper ID</th>
<th>Paper</th>
<th>L</th>
<th>T/P</th>
<th>Credits</th>
</tr>
</thead>
<tbody>
<tr>
<td>ETIC-401</td>
<td></td>
<td>Digital Control Systems</td>
<td>3</td>
<td>1</td>
<td>4</td>
</tr>
<tr>
<td>ETIC-403</td>
<td></td>
<td>Biomedical Instrumentation</td>
<td>3</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>ETIC-405</td>
<td></td>
<td>Artificial Neural Networks</td>
<td>3</td>
<td>1</td>
<td>4</td>
</tr>
</tbody>
</table>

**ELECTIVE (SELECT ANY TWO, ONE FROM EACH GROUP)**

### #GROUP-A

<table>
<thead>
<tr>
<th>Code No.</th>
<th>Paper ID</th>
<th>Paper</th>
<th>L</th>
<th>T/P</th>
<th>Credits</th>
</tr>
</thead>
<tbody>
<tr>
<td>ETIC-407</td>
<td></td>
<td>Industrial Automation and Control</td>
<td>3</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>ETAT-403</td>
<td></td>
<td>Mechatronics</td>
<td>3</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>ETEE-401</td>
<td></td>
<td>Electrical Drives</td>
<td>3</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>ETIC-413</td>
<td></td>
<td>Instrumentation Diagnostics</td>
<td>3</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>ETMT-415</td>
<td></td>
<td>Process Modeling and Optimization Techniques</td>
<td>3</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>ETCS-425</td>
<td></td>
<td>Database Management Systems</td>
<td>3</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>ETEE-419</td>
<td></td>
<td>Renewable Energy Resources</td>
<td>3</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>ETIC-417</td>
<td></td>
<td>Selected Topics in ICE**</td>
<td>3</td>
<td>0</td>
<td>3</td>
</tr>
</tbody>
</table>

### #GROUP-B

<table>
<thead>
<tr>
<th>Code No.</th>
<th>Paper ID</th>
<th>Paper</th>
<th>L</th>
<th>T/P</th>
<th>Credits</th>
</tr>
</thead>
<tbody>
<tr>
<td>ETIC-419</td>
<td></td>
<td>Engineering Materials</td>
<td>3</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>ETIC 421</td>
<td></td>
<td>Computer Architecture</td>
<td>3</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>ETIC 423</td>
<td></td>
<td>Software Engineering</td>
<td>3</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>ETIC 427</td>
<td></td>
<td>Operating Systems</td>
<td>3</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>ETHS 419</td>
<td></td>
<td>Sociology and Elements of Indian History for Engineers</td>
<td>3</td>
<td>0</td>
<td>3</td>
</tr>
</tbody>
</table>

**PRACTICAL/VIVA VOCE**

<table>
<thead>
<tr>
<th>Code No.</th>
<th>Paper ID</th>
<th>Paper</th>
<th>L</th>
<th>T/P</th>
<th>Credits</th>
</tr>
</thead>
<tbody>
<tr>
<td>ETIC 451</td>
<td></td>
<td>Digital Control Systems Lab</td>
<td>0</td>
<td>2</td>
<td>1</td>
</tr>
<tr>
<td>ETIC 453</td>
<td></td>
<td>Biomedical Instrumentation Lab</td>
<td>0</td>
<td>2</td>
<td>1</td>
</tr>
<tr>
<td>ETIC 455</td>
<td></td>
<td>Seminar (topic should be linked to industrial training/ Soft skills learnt)†</td>
<td>0</td>
<td>2</td>
<td>1</td>
</tr>
<tr>
<td>ETIC 461</td>
<td></td>
<td>Lab based on Electives Group A or B</td>
<td>0</td>
<td>2</td>
<td>1</td>
</tr>
<tr>
<td>ETIC 457</td>
<td></td>
<td>Minor Project+</td>
<td>0</td>
<td>6</td>
<td>3</td>
</tr>
<tr>
<td>ETIC 459</td>
<td></td>
<td>Industrial Training†</td>
<td>0</td>
<td>0</td>
<td>1</td>
</tr>
</tbody>
</table>

**TOTAL** 15 16 25

### #NUES

@ Industrial training was conducted after sixth semester. However, Viva-Voce for evaluation of Industrial Training will be conducted in this semester.

**Syllabus may be revised after 2 years.**

**Imp:** Elective Paper will be floated if one-third of the total students opt for the same. It is advised that the decision about the elective subject for 8th Semester is done before 15th November every year before end of seventh semester. New Electives may be added as per requirement after getting it duly approved by BOS and AC respectively.

† The student will submit a synopsis at the beginning of the semester for approval from the departmental committee in a specified format, thereafter he/she will have to present the progress of the work through seminars and progress reports.
## BACHELOR OF TECHNOLOGY
### (INSTRUMENTATION AND CONTROL ENGINEERING)
#### EIGHTH SEMESTER EXAMINATION

<table>
<thead>
<tr>
<th>Code No.</th>
<th>Paper ID</th>
<th>Paper</th>
<th>L</th>
<th>T/P</th>
<th>Credits</th>
</tr>
</thead>
<tbody>
<tr>
<td>ETIC-402</td>
<td></td>
<td>Human Values and Professional Ethics-II</td>
<td>1</td>
<td>0</td>
<td>1</td>
</tr>
<tr>
<td>ETIC-404</td>
<td></td>
<td>Intelligent Systems and Control</td>
<td>3</td>
<td>1</td>
<td>4</td>
</tr>
<tr>
<td>ETIC-406</td>
<td></td>
<td>Virtual Instrumentation</td>
<td>3</td>
<td>0</td>
<td>3</td>
</tr>
</tbody>
</table>

### ELECTIVE (SELECT ANY TWO, ONE FROM EACH GROUP)

#### GROUP-A

<table>
<thead>
<tr>
<th>Code No.</th>
<th>Paper ID</th>
<th>Paper</th>
<th>L</th>
<th>T/P</th>
<th>Credits</th>
</tr>
</thead>
<tbody>
<tr>
<td>ETIC-408</td>
<td></td>
<td>System Modelling and Simulation</td>
<td>3</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>ETIC-410</td>
<td></td>
<td>Embedded Systems</td>
<td>3</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>ETIC-418</td>
<td></td>
<td>Digital Image Processing</td>
<td>3</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>ETIC-414</td>
<td></td>
<td>VLSI Design</td>
<td>3</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>ETIC-408</td>
<td></td>
<td>Consumer Electronics</td>
<td>3</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>ETIC-418</td>
<td></td>
<td>Mobile Communication</td>
<td>3</td>
<td>0</td>
<td>3</td>
</tr>
</tbody>
</table>

#### GROUP-B

<table>
<thead>
<tr>
<th>Code No.</th>
<th>Paper ID</th>
<th>Paper</th>
<th>L</th>
<th>T/P</th>
<th>Credits</th>
</tr>
</thead>
<tbody>
<tr>
<td>ETMT-402</td>
<td></td>
<td>Robotics</td>
<td>3</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>ETEC-406</td>
<td></td>
<td>Ad hoc and Sensor Networks</td>
<td>3</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>ETCS-412</td>
<td></td>
<td>Object Oriented Software Engineering</td>
<td>3</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>ETIC-428</td>
<td></td>
<td>Computer Graphics and Multimedia</td>
<td>3</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>ETIC-430</td>
<td></td>
<td>Electromagnetic Field Theory</td>
<td>3</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>ETEC-404</td>
<td></td>
<td>Satellite Communication</td>
<td>3</td>
<td>0</td>
<td>3</td>
</tr>
</tbody>
</table>

### PRACTICAL/VIVA VOCE

<table>
<thead>
<tr>
<th>Code No.</th>
<th>Paper ID</th>
<th>Paper</th>
<th>L</th>
<th>T/P</th>
<th>Credits</th>
</tr>
</thead>
<tbody>
<tr>
<td>ETIC-452</td>
<td></td>
<td>Intelligent Systems and Control Lab</td>
<td>0</td>
<td>2</td>
<td>1</td>
</tr>
<tr>
<td>ETIC-454</td>
<td></td>
<td>Virtual Instrumentation Lab</td>
<td>0</td>
<td>2</td>
<td>1</td>
</tr>
<tr>
<td>ETIC-458</td>
<td></td>
<td>Lab based on Elective Group A or B</td>
<td>0</td>
<td>2</td>
<td>1</td>
</tr>
<tr>
<td>ETIC-456</td>
<td></td>
<td>*Major Project</td>
<td>0</td>
<td>12</td>
<td>8</td>
</tr>
</tbody>
</table>

**TOTAL**

<table>
<thead>
<tr>
<th></th>
<th></th>
<th></th>
<th>L</th>
<th>T/P</th>
<th>Credits</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td>13</td>
<td>19</td>
<td>25</td>
</tr>
</tbody>
</table>

*Note:*

1. The student will submit a synopsis at the beginning of the semester for approval from the departmental committee in a specified format, thereafter he/she will have to present the progress of the work through seminars and progress reports. Seminar related to major project should be delivered one month after starting of Semester. The progress will be monitored through seminars and progress reports.

2. Syllabus may be revised after 2 years.

---

**NOTE:**

1. The total number of the credits of the B.Tech. (ICE) Programme = 215.
2. Student shall be required to appear in examinations of all courses. However, to award the degree a student shall be required to earn the minimum of 200 credits including Mandatory papers (M).

---

**FOR LATERAL ENTRY STUDENTS:**

1. The total number of the credits of the B.Tech. (ICE) Programme = 161.
2. Each student shall be required to appear for examinations in all courses Third Semester onwards. However, for the award of the degree a student shall be required to earn the minimum of 150 credits including mandatory papers (M).

---

Scheme and Syllabi for B. Tech-ICE, 1st year (Common to all branches) w.e.f batch 2014-15 and (2nd, 3rd & 4th years) w.e.f batch 2013-14 approved in the 22nd BOS of USET on 30th June, 2014 and approved in the 37th AC Sub Committee Meeting held on 10th July, 2014.
NOMENCLATURE OF CODES GIVEN IN THE SCHEME OF
B. TECH AND M. TECH

1. ET stands for Engineering and Technology.
2. PE stands for Power Engineering.
3. ME stands for Mechanical Engineering.
4. MT stands for Mechatronics.
5. AT stands for Mechanical and Automation Engineering.
6. EE stands for Electrical and Electronics Engineering.
7. EL stands for Electrical Engineering.
8. IT stands for Information Technology
9. CS stands for Computer Science and Engineering
10. CE stands for Civil Engineering
11. EC stands for Electronics and Communications Engineering.
12. EN stands for Environmental Engineering
13. TE stands for Tool Engineering
14. MA stands for Mathematics
15. HS stands for Humanities and Social Sciences
16. SS stands for Social Services
APPLIED MATHEMATICS-III

Paper Code: ETMA-201
Paper: Applied Mathematics-III
L T/P C
3 1 4

INSTRUCTIONS TO PAPER SETTERS:

1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.
2. Apart from Question No. 1, rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be 12.5 marks.

Objectives: The objective of this course is to teach the students the applications of fourier series, fourier transform, difference equation and numerical methods to solve various engineering problems.

UNIT-I
Fourier series: Definition, Euler’s formula, conditions for Fourier expansion, functions having points of discontinuity, even and odd functions, half range series, Harmonic analysis. Fourier Transforms: Definition, Fourier integral, Fourier transform, inverse Fourier transform, Fourier sine and cosine transforms, properties of Fourier transforms (linearity, scaling, shifting, modulation), Application to partial differential equations.

UNIT-II
Difference equation: Definition, formation, solution of linear difference equation with constant coefficients, simultaneous difference equations with constant coefficients, applications of difference equations. Z-transform: Definition, Z-transform of basic functions, properties of Z-transform (linearity, damping, shifting, multiplication), initial value theorem, final value theorem, convolution theorem, convergence of Z-transform, inverse of Z-transform, Application to difference equations.

UNIT-III

UNIT-IV

Text Books:

Reference Books:
[R5] Schaum’s Outline on Fourier Analysis with Applications to Boundary Value Problem, Tata McGraw-Hill

GURU GOBIND SINGH INSTITUTE OF TECHNOLOGY
SENORS AND TRANSDUCERS

Paper Code: ETIC-203
Paper: Sensors and Transducers

<table>
<thead>
<tr>
<th>L</th>
<th>T/P</th>
<th>C</th>
</tr>
</thead>
<tbody>
<tr>
<td>3</td>
<td>1</td>
<td>4</td>
</tr>
</tbody>
</table>

INSTRUCTIONS TO PAPER SETTERS:
MAXIMUM MARKS: 75
1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.
2. Apart from Question No. 1, rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks.

Objective:-To provide the basic understanding about operational characteristics and applications of various sensors and transducers.

UNIT I [Introduction to Sensors]
Definition and differences of sensors and transducers, Classification, static and dynamic characteristics, electrical characterization, mechanical and thermal characterization including bath-tub curve.

Different Sensors:

UNIT-II
Thermal sensors: Gas thermometric sensors, Dielectric constant, refractive index thermo-sensors, nuclear thermometers, resistance change type thermometric sensors, Thermoeffect sensors.
Magnetic sensors: Basic working principles, Magnetostriuctive, Hall effect, Eddy current type, SQUID sensors.
Radiation sensors: Photo-detectors, Photo-emissive, photomultiplier, scintillation detectors.

UNIT-III

UNIT-IV

Text Books:

Reference Book:
SWITCHING THEORY AND LOGIC DESIGN

Paper Code: ETEC-205
Paper: Switching Theory and Logic Design

L T/P C
3 1 4

INSTRUCTIONS TO PAPER SETTERS:

1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.
2. Apart from Question No. 1, rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks.

MAXIMUM MARKS: 75

Objective: The objective of the paper is to facilitate the student with the knowledge of Logic Systems and Circuits, thereby enabling the student to obtain the platform for studying Digital Systems and Computer Architecture.

UNIT- I
Number Systems and Codes: - Decimal, Binary, Octal and Hexadecimal Number systems, Codes- BCD, Gray Code, Excess-3 Code, ASCII, EBCDIC, Conversion between various Codes.
Switching Theory: - Boolean Algebra- Postulates and Theorems, De’ Morgan’s Theorem, Switching Functions-Canonical Forms- Simplification of Switching Functions- Karnaugh Map and Quine Mc-Clusky Methods.
Combinational Logic Circuits: - Review of basic gates- Universal gates, Adder, Subtractor, Serial Adder, Parallel Adder, Carry Propagate Adder, Carry Look-ahead Adder, Carry Save Adder, Comparators, Parity Generators, Decoder and Encoder, Multiplexer and De-multiplexer, ALU, PLA and PAL.

[T2,T3][No. of hrs. 14]

UNIT- II
Integrated circuits: - TTL and CMOS logic families and their characteristics. Brief introduction to RAM and ROM.
Counters and Shift Registers: - Design of Synchronous and Asynchronous Counters: - Binary, BCD, Decade and Up/Down Counters , Shift Registers, Types of Shift Registers, Counters using Shift Registers- Ring Counter and Johnson Counter.

[T2,T3][No. of hrs. 10]

UNIT- III
Finite state machine-capabilities and limitations, Mealy and Moore models-minimization of completely specified and incompletely specified sequential machines, Partition techniques and merger chart methods-concept of minimal cover table.

[T1][No. of hrs. 10]

UNIT- IV
Algorithmic State Machine: Representation of sequential circuits using ASM charts synthesis of output and next state functions, Data path control path partition-based design.
Fault Detection and Location: Fault models for combinational and sequential circuits, Fault detection in combinational circuits; Homing experiments, distinguishing experiments, machine identification and fault detection experiments in sequential circuits.

[T1][No. of hrs. 10]

Text Book:

Reference Books:
CIRCUITS & SYSTEMS

Paper Code: ETEE-207  
Paper: Circuits & Systems

L  T/P  C
3  1  4

INSTRUCTIONS TO PAPER SETTERS:

1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.
2. Apart from Q. No.1 rest of the paper shall consist of four units as per the syllabus, every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be 12.5 marks.

Objective: The purpose of this course is for each student to learn and further explore the techniques of advanced circuit analysis. The concepts and analytical techniques gained in this course (e.g., signals, Laplace transformation, frequency response) will enable students to build an essential foundation of many fields within electrical engineering, such as control theory, analog electronic circuits, signal processing.

UNIT-I
Introduction to signals, their classification and properties, different types of systems, LTI systems and their properties, periodic waveforms and signal synthesis, properties and applications of Laplace transform of complex waveform.

[No. of Hours: 10]

UNIT-II

[No. of Hours: 12]

UNIT-III

[No. of Hours: 10]

UNIT IV
Positive real function and synthesis of LC, RC, RL Networks in Foster’s I and II, Cauer’s I & II forms, Introduction of passive filter and their classification, frequency response, characteristic impedance of low pass, high pass, Band Pass and Band reject prototype section.

[No. of Hours: 10]

Text Books:

Reference Books:

Scheme and Syllabi for B. Tech-ICE, 1st year (Common to all branches) w.e.f batch 2014-15 and (2nd, 3rd & 4th years) w.e.f batch 2013-14 approved in the 22nd BOS of USET on 30th June, 2014 and approved in the 37th AC Sub Committee Meeting held on 10th July, 2014.
DATA STRUCTURES

Paper Code: ETCS-209
Paper: Data Structures

L T/P C
3 1 4

INSTRUCTIONS TO PAPER SETTERS:

1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.
2. Apart from Question No. 1, the rest of the paper shall consist of four units as per the syllabus. Each unit should have two questions. However, the student may be asked to attempt only 1 question from each unit. Each question should be 12.5 marks.

Objective: To understand the programming and the various techniques for enhancing the programming skills for solving and getting efficient results.

UNIT – I:
Introduction to programming methodologies and design of algorithms. Abstract Data Type, array, array organization, sparse array. Stacks and Stack ADT, Stack Manipulation, Prefix, infix and postfix expressions, their interconversion and expression evaluation. Queues and Queue ADT, Queue manipulation. General Lists and List ADT, List manipulations, Single, double and circular lists.

UNIT – II:
Trees, Properties of Trees, Binary trees, Binary Tree traversal, Tree manipulation algorithms, Expression trees and their usage, binary search trees, AVL Trees, Heaps and their implementation.

UNIT – III:
Multiway trees, B-Trees, 2-3 trees, 2-3-4 trees, B* and B+ Trees, Graphs, Graph representation, Graph traversal.

UNIT – IV:
Sorting concept, order, stability, Selection sorts (straight, heap), insertion sort (Straight Insertion, Shell sort), Exchange Sort (Bubble, quicksort), Merge sort (only 2-way merge sort). Searching - List search, sequential search, binary search, hashing concepts, hashing methods (Direct, subtraction, modulo-division, midsquare, folding, pseudorandom hashing), collision resolution (by open addressing: linear probe, quadratic probe, pseudorandom collision resolution, linked list collision resolution), Bucket hashing.

Text Books:

Reference Books:
[R2] Tanenbaum; “Data Structures using C”, Pearson/PHI.
BASICS OF MEASUREMENTS

Paper Code: ETIC-211
Paper: Basics of Measurements
L T/P C 3 1 4

INSTRUCTIONS TO PAPER SETTERS:

1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.
2. Apart from Q. No.1 rest of the paper shall consist of four units as per the syllabus, every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be 12.5 marks.

Objective: To provide the basic understanding about the importance of measurement, information about different types of instruments and gadgets used for measurement and their characteristics besides some of its standards and calibration methods.

UNIT-I
Introduction to Measurement:
Significance of measurement, Different methods of measurement, Classification of measuring instruments, Application of measurement systems, typical measurement schemes.

Units and Standards:
MKS, SI units of engineering parameters, Details of different standards—mass, length, time, frequency, temperature, EMF, ampere, sub standards and lab standards.

Performance Characteristics:
Definition of range, span, accuracy, precision, drift, sensitivity, reproducibility, repeatability, dead zone, resolution, hysteresis, threshold, zero error, noise, linearity, loading effect, static characteristics.

UNIT-II
Testing & Calibration of measurement setup:
Dynamic Characteristics:
Dynamic response; Transient response; speed of response, fidelity, measuring lag etc., Linear approximation, Introduction to compensation techniques.

Significance of testing and calibration, Calibration curve, Standards for calibration, Different calibration procedures—primary, secondary, direct, indirect, routine calibration, Calibration setup:—pressure gauge, level etc. Calibration of Ammeter, Voltmeter and Wattmeter, Energy meter.

UNIT-III
Analysis of Errors
Definition; Types of errors; Calculation methods of different errors; Gaussian curve; Precision Index; Variance; Standard deviation; Uncertainty in measurement, Chi-Square Test, Curve fitting methods.

Galvanometers:
D’Arsonaval Galvanometer— construction, Torque equation, Dynamic characteristic, Balastic Galvanometer—construction, working principle.

UNIT-IV
Displays and Recorders:
Indicating Instruments- Construction, Operating principle of spring control, gravity control and damping.

Recorders- Working Principle of chart recorder, strip chart, circular chart, magnetic tape recorder, thermal recorders, printer.

Electronic Display- LCD, LED, alphanumeric, storage Oscilloscope.

Text Books:

[T2] Albert D.Helfrick, William D.Cooper, “Modern Electronic Instrumentation and Measurement Techniques”, PHI India

Scheme and Syllabi for B. Tech-ICE, 1st year (Common to all branches) w.e.f batch 2014-15 and (2nd, 3rd & 4th years) w.e.f batch 2013-14 approved in the 22nd BOS of USET on 30th June, 2014 and approved in the 37th AC Sub Committee Meeting held on 10th July, 2014.
Reference Books:

Scheme and Syllabi for B. Tech-ICE, 1st year (Common to all branches) w.e.f batch 2014-15 and (2nd, 3rd & 4th years) w.e.f batch 2013-14 approved in the 22nd BOS of USET on 30th June, 2014 and approved in the 37th AC Sub Committee Meeting held on 10th July, 2014.
SENSORS AND TRANSDUCERS LAB

Paper Code: ETIC-251
Paper: Sensors and Transducers Lab

<table>
<thead>
<tr>
<th>L</th>
<th>T/P</th>
<th>C</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>2</td>
<td>1</td>
</tr>
</tbody>
</table>

List of Experiments:
1. Study of various sensors e.g., Thermocouple, RTD, Thermistor, Magnetic Sensors, Load Cells, Film Sensors.
2. Characteristics of (Resistive and Thermo emf) temperature sensor
3. Measurement of displacement using LVDT
4. Measurement of strain and torque using strain gauges
5. Measurement of speed using photoelectric sensors, tachogenerators and stroboscope.
6. Calibration and measurement of temperature using PRT.
7. Static and Dynamic Characteristics of sensors.
8. Liquid level measurement using capacitive measurement system.
9. Pressure measurement using load cell.
10. Study and operation of Electrochemical Cell.

NOTE:- At least 8 Experiments out of the list must be done in the semester.
SWITCHING THEORY AND LOGIC DESIGN LAB

Paper Code: ETEC-253
Paper: Switching Theory and Logic Design Lab

<table>
<thead>
<tr>
<th>L</th>
<th>T/P</th>
<th>C</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>2</td>
<td>1</td>
</tr>
</tbody>
</table>

List of Experiments:

1. Realize all gates using NAND & NOR gates
2. Realize Half Adder, Full Adder, Half subtractor, Full subtractor
3. Realize a BCD adder
4. Realize a Serial Adder
5. Realize a four bit ALU
6. Realize Master-Save J K Flip-Flop, using NAND/NOR gates
7. Realize Universal Shift Register
8. Realize Self-Starting, Self Correcting Ring Counter
9. Realize Multiplexer and De-Multiplexer
10. Realize Carry Look ahead Adder / Priority Encoder
11. Simulation of PAL and PLA
12. Simulation Mealy and Moore State machines

NOTE: - At least 8 Experiments out of the list must be done in the semester
**DATA STRUCTURES LAB**

**Paper Code:** ETCS-255

**Paper:** Data Structures Lab

<table>
<thead>
<tr>
<th>L</th>
<th>T/P</th>
<th>C</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>2</td>
<td>1</td>
</tr>
</tbody>
</table>

**List of Experiments:**

1. Perform Linear Search and Binary Search on an array.
   - **Description of programs:**
     a. Read an array of type integer.
     b. Input element from user for searching.
     c. Search the element by passing the array to a function and then returning the position of the element from the function else return -1 if the element is not found.
     d. Display the position where the element has been found.

2. Implement sparse matrix using array.
   - **Description of program:**
     a. Read a 2D array from the user.
     b. Store it in the sparse matrix form, use array of structures.
     c. Print the final array.

3. Create a linked list with nodes having information about a student and perform
   - I. Insert a new node at specified position.
   - II. Delete of a node with the roll number of student specified.
   - III. Reversal of that linked list.

4. Create doubly linked list with nodes having information about an employee and perform Insertion at front of doubly linked list and perform deletion at end of that doubly linked list.

5. Create circular linked list having information about an college and perform Insertion at front perform Deletion at end.

6. Create a stack and perform Pop, Push, Traverse operations on the stack using Linear Linked list.

7. Create a Linear Queue using Linked List and implement different operations such as Insert, Delete, and Display the queue elements.

8. Create a Binary Tree (Display using Graphics) perform Tree traversals (Preorder, Postorder, Inorder) using the concept of recursion.

9. Implement insertion, deletion and display (inorder, preorder and postorder) on binary search tree with the information in the tree about the details of a automobile (type, company, year of make).

10. To implement Insertion sort, Merge sort, Quick sort, Bubble sort, Bucket sort, Radix sort, Shell sort, Selection sort, Heap sort and Exchange sort using array as a data structure.

**NOTE:** At least 8 Experiments out of the list must be done in the semester.
CIRCUITS AND SYSTEMS LAB

Paper Code: ETEE-257 L T/P C
Paper: Circuits and Systems Lab 0 2 1

List of Experiments

1. Study the transient response of series RLC circuit for different types of waveforms on CRO and verify using MATLAB
2. Study the time response of a simulated linear system and verify the unit step and square wave response of first order and second order, type 0.1 system
3. Using MATLAB determine current in various resistors connected in network using mesh current and node voltage analysis.
4. To determine Z and Y parameters of the given two port network.
5. To determine ABCD parameters of the given two port network.
6. To verify Reciprocity Theorem for the given two port network.
7. To determine Hybrid parameters of the given two port network.
8. To design Cascade Connection and determine ABCD parameters of the given two port network.
9. To design Series-Series Connection and determine Z parameters of the given two port network.
10. To design Parallel-Parallel Connection and determine Y parameters of the given two port network.
11. To design Series-Parallel Connection and determine h parameters of the given two port network.
12. Study the frequency response of different filter circuits.

NOTE:- At least 8 Experiments out of the list must be done in the semester.
MEASUREMENTS AND INSTRUMENTATION

Paper Code: ETIC-204
Paper: Measurements and Instrumentation

<table>
<thead>
<tr>
<th>L</th>
<th>T/P</th>
<th>C</th>
</tr>
</thead>
<tbody>
<tr>
<td>3</td>
<td>0</td>
<td>3</td>
</tr>
</tbody>
</table>

INSTRUCTIONS TO PAPER SETTERS:

Maximum Marks: 75

1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.
2. Apart from Q. No. 1 rest of the paper shall consist of four units as per the syllabus, every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be 12.5 marks.

Objective:- To provide the basic understanding regarding ac measurements and instrumentation, working principles of associated meters and instrumentation schemes.

UNIT - I
Potentiometer and Bridges:
A. C. Bridges: Configurations, Errors and accuracies, different types of bridges and their application, De Sauty Bridge, Schering Bridge, Anderson Bridge, Maxwell Bridge, Wein Bridge, Use of Shielding in Bridges, Wagner Earth Connection, Grounding and Guarding.

UNIT - II
Instrument Transformers:
Construction, operation, ratio and phase errors in current transformers, compensation techniques for errors in current transformers, testing of current transformers, absolute and comparison methods, Construction, operation, ratio and phase errors in potential transformers, compensation techniques for errors in potential transformers, testing of potential transformers.

UNIT - III
AC instruments and Meters:
Induction type instruments; Theory, operation, adjustments and calibration of single phase energy meter, Polyphase energy meter, Ampere Hour Meters, Measurement of Volt-ampere and reactive volt amperes, Power Factor Meters, Frequency Meters, Synchroscopes, Phase sequence Indicators, maximum demand meters. Regulated Power Supplies, Function Generator: Sine, Cosine, Square and triangular wave, Instrumentation amplifier and their applications.

UNIT - IV
Electronic Measuring Instruments:
General purpose Cathode Ray Oscilloscope; Construction & working, principles, various controls, applications in measurement, Digital storage Oscilloscope (DSO).

Text Books:
[T2] Albert D.Helfrick,William D.Cooper, “Modern Electronic Instrumentation and Measurement Techniques”, PHI India,

Reference Books:
[R3] Kishore-Electronic Instrumentation and Measurement”, Pearson
CONTROL SYSTEMS

Paper Code: ETEE-212
Paper: Control Systems
L T/P C
3 1 4

INSTRUCTIONS TO PAPER SETTERS:

Maximum Marks: 75

1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.
2. Apart from Q. No.1 rest of the paper shall consist of four units as per the syllabus, every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be 12.5 marks.

Objective: To teach the fundamental concepts of Control systems and mathematical modeling of the system. To study the concept of time response and frequency response of the system. To teach the basics of stability analysis of the system

Unit I: Control Systems - Basics & Components
Introduction to basic terms, classifications & types of Control Systems, block diagrams & signal flow graphs. Transfer function, determination of transfer function using block diagram reduction techniques and Mason’s Gain formula.
Control system components: Electrical, Mechanical, Electronic, A.C., D.C. types, Servo Motors, Stepper Motors, Tacho Generators, Synchros, Magnetic Amplifiers, Servo Amplifiers.

[T1,T2][No. of Hrs. 11]

Unit II: Time – Domain Analysis
Time domain performance specifications, transient response of first & second order systems, steady state errors and static error constants in unity feedback control systems, response with P, PI and PID controllers, limitations of time domain analysis.

[T1,T2][No. of Hrs. 10]

Unit III : Frequency Domain Analysis
Polar and inverse polar plots, frequency domain specifications and performance of LTI systems, Logarithmic plots (Bode plots), gain and phase margins, relative stability. Correlation with time domain/performance close loop frequency responses from open loop response. Limitations of frequency domain analysis, minimum/non-minimum phase systems.

[T1,T2][No. of Hrs. 10]

Unit IV : Stability & Compensation Techniques

[T1,T2][No. of Hrs. 11]

Text Books:

Reference Books:
Objective: The objective of the paper is to facilitate the student with the basics of Power Electronics that are required for an engineering student.

UNIT- I
Introduction
Characteristics and switching behaviour of Power Diode, SCR, UJT, TRIAC, DIAC, GTO, MOSFET, IGBT, MCT and power BJT, two-transistor analogy of SCR, firing circuits of SCR and TRIAC, SCR gate characteristics, SCR ratings. Protection of SCR against over current, over voltage, high dV/dt, high dI/dt, thermal protection, Snubber circuits, Methods of commutation, series and parallel operation of SCR, Driver circuits for BJT/MOSFET.

UNIT- II
A.C. to D.C. Converter: Classification of rectifiers, phase controlled rectifiers, fully controlled and half controlled rectifiers and their performance parameters, three phase half wave, full wave and half controlled rectifiers and their performance parameters, effect of source impedance on the performance of single phase and three phase controlled rectifiers, single-phase and three phase dual converter.

UNIT- III
D.C. to D.C. Converter: Classification of choppers as type A, B, C, D and E, principle of operation, switching mode regulators: Buck, Boost, Buck-Boost, Cuk regulators.
A.C. to A.C. Converter: AC voltage Controllers, Cyclo-converters: single phase to single phase, three phase to single phase, three phase to three phase Cyclo-converter circuit and their operation, Matrix converter.

UNIT-IV
D.C. to A.C. Converter: single phase single pulse inverter: Square wave, quasi square. Three phase single pulse inverters (120° and 180° conduction) Modulation Techniques and reduction of harmonics, PWM techniques, SPWM techniques, SVM, Carrier less modulation, PWM Inverter, Bidirectional PWM converters, voltage source inverters and current source inverter, Multi level Inverter, cascaded and NPC inverters.

Text Books:

References Books:
[R2] Ned Mohan, Tore M. Undeland and Robbins, “Power Electronics: Converters, Applications and Design” Wiley India Publication
# THEORY AND APPLICATION OF INTEGRATED CIRCUITS

**Paper Code:** ETIC-208  
**Paper:** Theory and Application of Integrated Circuits  
**L T/P C:** 3 1 4

**INSTRUCTIONS TO PAPER SETTERS:**  
**MAXIMUM MARKS:** 75

1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.

2. Apart from question no. 1, rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks.

**Objective:** The objective of the paper is to facilitate the students with the knowledge of solid state device with theoretically infinite number of operating states or infinite discrete I/O states. And all the advantages of different transistor configuration is put into a single IC Op-amp.

## UNIT- I: Fundamentals of IC Fabrication and Circuit Configurations for Linear IC
- IC classification, fundamental of monolithic IC technology, epitaxial growth, masking and etching, diffusion of impurities.
- Realization of monolithic ICs and packaging.
- Differential Amplifier, Differential Amplifier Configurations, Bipolar differential Amplifier, AC and DC characteristics.
- Current mirror and BJT Current Source. Current source as Active load.
- Voltage Sources, voltage reference. Operational Amplifiers IC 741, DC and AC performance characteristics, Open and closed loop configurations.

(T1, T2) [No. of Hrs. 11]

## UNIT- II: Applications of Operational Amplifiers
- Inverting and non inverting Amplifier, Scale Changer, Phase Shift Circuits, Voltage Follower, V-to-I and I-to-V converters, adder, subtractor.
- Instrumentation amplifier, Integrator, Differentiator, Logarithmic amplifier, Antilogarithmic amplifier, Comparators, Schmitt trigger.
- Precision rectifier, peak detector, clipper and clamper.
- Low-pass, high-pass and band-pass Butterworth filters.

(T1, T2) [No. of Hrs. 11]

## UNIT- III: Analog Multiplier, PLL, A/D and D/A Convertor
- Analog Multiplier using Emitter Coupled Transistor Pair – Gilbert Multiplier cell – Variable transconductance technique.
- analog multiplier ICs and their applications, Operation of the basic PLL, Closed loop analysis.

(T1, T2) [No. of Hrs. 11]

## UNIT- IV: Waveform Generators and Special Function ICs
- Sine-wave generators, Multivibrators and Triangular wave generator, Saw-tooth wave generator.
- ICL8038 function generator, Timer IC 555, IC Voltage regulators – Three terminal fixed and adjustable voltage regulators – IC 723 general purpose regulator – Monolithic switching regulator, Switched capacitor filter IC MF10, Frequency to Voltage and Voltage to Frequency converters, Audio Power amplifier, Video Amplifier, Opto-couplers.

(T1, T2) [No. of Hrs. 11]

**Text Books:**

**Reference Books:**
ELECTRICAL MACHINES

Paper Code: ETIC-210
Paper: Electrical Machines

<table>
<thead>
<tr>
<th>L</th>
<th>T/P</th>
<th>C</th>
</tr>
</thead>
<tbody>
<tr>
<td>3</td>
<td>1</td>
<td>4</td>
</tr>
</tbody>
</table>

INSTRUCTIONS TO PAPER SETTERS:

1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.
2. Apart from Question No. 1, rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks.

MAXIMUM MARKS: 75

Objective: The objective of this paper is to make students understand principle of electromechanical energy conversion and electrical machines.

DC machines: construction, armature windings, induced EMF equation, torque production, magnetization curve. Types of generators and motors, characteristics, commutation and interpoles, armature reaction, Speed control of dc motor and starting.
PMDC machine: Introduction and need of brushless motors.

UNIT- II: Transformers: construction, ideal and practical transformer, equivalent circuits, voltage regulation, maximum efficiency criterion. Open circuit and short circuit tests. Phasor diagrams on no load, full load, lagging and leading power factor loads. Three phase transformer.
Introduction to polyphase induction machines, production of rotating magnetic flux vector, principle of operation, importance of air gap, comparison with transformer, types of rotor.

Single phase induction motor, double field revolving theory, starting methods of single phase induction motors, universal motor and introduction to switched reluctance motor.


Text books:

Reference Books:
[R1] M. V. Deshpande, “Electrical Machines” PHI
COMMUNICATION SYSTEMS

Paper Code: ETIC-212
Paper: Communication Systems

L T/P C
3 1 4

INSTRUCTIONS TO PAPER SETTERS:

Objective: This is the first course which introduces the concepts of communication systems, channels and various analog modulation methods. Further, an insight into the behavior of noise is dealt.

UNIT I
Introduction: Overview of Communication system, Communication channels, Mathematical Models for Communication Channels
Introduction of random Variables: Definition of random variables, PDF, CDF and its properties, joint PDF, CDF, Marginalized PDF, CDF, WSS wide stationery, strict sense stationery, non stationery signals, UDF, GDF, RDF, Binomial distribution, White process, Poisson process, Wiener process.

UNIT II
Pulse Analog Modulation: Sampling-Natural and Flat top. reconstruction, TDM-Pulse Amplitude Modulation (TDM-PAM), Pulse Width Modulation (PWM), Pulse Position Modulation(PPM), Generation and Recovery.
Pulse Digital Modulation: Pulse Code Modulation (PCM), Differential Pulse Code Modulation (DPCM), Delta Modulation (DM), ADPCM.

UNIT III
Information and Coding Theory: Entropy, Information, Channel Capacity. Source Coding Theorem: Shannon Fano Coding, Huffman Coding.

UNIT IV

Text Books:

Reference Books:
MEASUREMENTS AND INSTRUMENTS LAB

Paper Code: ETIC-252        L     T/P     C
Paper: Measurements and Instruments Lab                  0     2     1

List of Experiments:

1. To measure inductance using Maxwell / Anderson Bridge.
2. To measure Capacitance using Schering Bridge.
3. To measure the low resistance using Kevin Double Bridge.
4. To perform calibration of single phase energy meter (Analog Vs. Digital).
5. Unknown voltage measurement using potentiometer.
6. To measure frequency and Phase of various signals using CRO.
7. Testing of phase error and ratio error of current transformer (CT).
8. Study the characteristics of instrumentation amplifier.
9. Study the frequency response of passive filters.

NOTE:- At least 8 Experiments out of the list must be done in the semester.
CONTROL SYSTEMS LAB

Paper Code: ETEE-260
Paper: Control Systems Lab

<table>
<thead>
<tr>
<th>L</th>
<th>T/P</th>
<th>C</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>2</td>
<td>1</td>
</tr>
</tbody>
</table>

LIST OF EXPERIMENTS

1. Comparison of open loop & closed loop control in speed control of D.C. motor & to find the transfer function.
2. To study the characteristics of positional error detector by angular displacement of two servo potentiometers
   a. excited with dc
   b. excited with ac
3. To study synchro transmitter in terms of position vs phase and voltage magnitude with respect to rotor voltage magnitude /phase.
4. To study remote position indicator systems using synchro transmitter/receiver.
5. To plot speed- torque curves for ac servomotor for different voltages.
6. To study ac motor position control system & to plot the dynamic response & calculate peak time, settling time, peak overshoot, damping frequency, steady state error etc.
7. To study the time response of simulated linear systems.
8. To study the performance of PID Controller.
9. Plot impulse response, unit step response, unit ramp response of any 2nd order transfer function on same graph using MATLAB.
10. To draw the magnetization (Volt Amps) characteristics of the saturable core reactor used in the magnetic amplifier circuits.
11. Plot root locus for any 2nd order system (with complex poles). For Mp=30%, find the value of K using MATLAB.
12. To design lead-lag compensator for the given process using Bode plots in MATLAB.

NOTE: At least 8 Experiments out of the list must be done in the semester.
### POWER ELECTRONICS LAB

**Paper Code:** ETIC-256  
**Paper:** Power Electronics Lab  
**L** | **T/P** | **C**
---|---|---
0 | 2 | 1

**LIST OF EXPERIMENTS**

1. To study and analyze V-I characteristics of SCR and TRIAC.
2. To study the switching characteristics of MOSFET and IGBT.
3. To study R and RC and UJT based firing circuits using SCR.
4. To study single phase Semi-converter and Full converters feeding R and RL load.
5. To study A.C phase control using SCR (half and full wave) using DIAC and TRIAC for dimmer application.
6. To study single-phase cyclo-converter feeding R and RL loads.
7. To study the operation and duty cycle control of buck and boost converter feeding R loads.
8. To study the operation and duty cycle control of Type-C chopper.
9. To study the THD in operation of single phase Square wave and Quasi square wave Inverter.
10. To study the operation of SPWM Inverter.

**NOTE:** At least 8 Experiments out of the list must be done in the semester.
THEORY AND APPLICATION OF INTEGRATED CIRCUITS LAB

Paper Code: ETIC-258
Paper: Theory and Application of Integrated Circuits Lab

List of Experiments:

1. Measure the DC characteristics of Operational amplifier 741.
2. To construct and test the performance of an Inverting, Non-inverting amplifier and Differential amplifier using IC μA 741.
3. To construct and test the performance of an Integrator and Differentiator using IC μA 741.
4. To design and verify the operation of instrumentation amplifier using IC μA 741.
5. To design and verify the operation of the Active low pass, High pass and Band pass filters using IC μA 741.
6. To design and construct an Astable, Monostable multivibrators and Schmitt trigger using IC μA 741.
7. To design and test RC phase shift and Wien bridge oscillators using IC μA 741.
8. To design and construct an Astable and Monostable multivibrators using NE555 Timer.
9. To design and construct a PLL Characteristics and Frequency multiplier using NE 565.
10. To design and construct a DC Power Supply using LM317 and LM78XX and LM79XX.

NOTE:- At least 8 Experiments out of the list must be done in the semester.
ELECTRICAL MACHINE LAB

Paper Code: ETIC-254
Paper: Electrical Machine Lab

L T/P C
0 2 1

List of Experiments

1. To obtain magnetization characteristics of DC shunt generator and determine critical field resistance and critical speed.
2. To perform load test on DC shunt generator and determine the characteristics.
3. To perform speed control of DC shunt motor by field and armature control.
4. To perform Open circuit and short circuit tests on single phase transformer for parameter estimation of the transformer.
5. To obtain star-star, star-delta and delta-delta connections for three phase transformers.
6. To perform parallel operation of two single phase transformers with non linear load.
7. To perform block rotor test and no load test on induction motor (three phase) for parameter estimation.
8. To perform SCC and OCC of an alternator and calculate voltage regulation at UPF, .8 leading and .8 lagging pf.

NOTE:- At least 8 Experiments out of the list must be done in the semester.
COMMUNICATION SKILLS FOR PROFESSIONALS

Paper Code: ETHS-301
Paper: Communication Skills for Professionals

L  T/P  C
2  0  1

INSTRUCTIONS TO PAPER SETTERS:

1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.

2. Apart from Question No. 1, rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks.

Maximum Marks: 75

Objective: To develop communication competence in prospective engineers so that they are able to communicate information as well as their thoughts and ideas with clarity and precision. This course will also equip them with the basic skills required for a variety of practical applications of communication such as applying for a job, writing reports and proposals. Further, it will make them aware of the new developments in communication that have become part of business organisations today.

UNIT I
Organizational Communication: Meaning, importance and function of communication, Process of communication, Communication Cycle - message, sender, encoding, channel, receiver, decoding, feedback, Characteristics, Media and Types of communication, Formal and informal channels of communication, 7 C’s of communication, Barriers to communication, Ethics of communication (plagiarism, language sensitivity)

UNIT II
Introduction to Phonetics: IPA system (as in Oxford Advanced Learner’s Dictionary), Speech Mechanism, The Description of Speech Sounds, Phoneme, Diphthong, Syllable, Stress, Intonation, Prosodic Features; Pronunciation; Phonetic Transcription - Conversion of words to phonetic symbols and from phonetic symbols to words. British & American English (basic difference in vocabulary, spelling, pronunciation, structure)
Non-Verbal Language: Importance, characteristics, types – Paralanguage (voice, tone, volume, speed, pitch, effective pause), Body Language (posture, gesture, eye contact, facial expressions), Proxemics, Chronemics, Appearance, Symbols.

UNIT III
Letters at the Workplace – letter writing (hard copy and soft copy): request, sales, enquiry, order, complaint.
Job Application -- resume and cover letter
Meeting Documentation-- notice, memo, circular, agenda and minutes of meeting.

UNIT IV
Listening and Speaking Skills: Importance, purpose and types of listening, process of listening, difference between hearing and listening, Barriers to effective listening. Traits of a good listener, Tips for effective listening. Analytical thinking; Speech, Rhetoric, Polemics; Audience analysis. Telephone Skills - making and receiving calls, leaving a message, asking and giving information, etiquettes.
Presentations: Mode, mean and purpose of presentation, organizing the contents, nuances of delivery, voice and body language in effective presentation, time dimension.
Group Discussion: Purpose, types of GDs, strategies for GDs, body language and guidelines for group discussion.
Interview Skills: Purpose, types of interviews, preparing for the interview, attending the interview, interview process, employers expectations, general etiquettes.
Text Books:

References Books:
INDUSTRIAL INSTRUMENTATION

Paper Code: ETIC-303
Paper: Industrial Instrumentation

<table>
<thead>
<tr>
<th>INSTRUCTIONS TO PAPER SETTERS:</th>
<th>MAXIMUM MARKS: 75</th>
</tr>
</thead>
<tbody>
<tr>
<td>1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.</td>
<td></td>
</tr>
<tr>
<td>2. Apart from Question No. 1, rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks.</td>
<td></td>
</tr>
</tbody>
</table>

Objective: To familiarize the students with the measurement schemes used for monitoring various analytical and non electrical parameters encountered in industrial applications.

UNIT-I

Temperature Measurements: Importance, advantage and limitation of different instruments, Seeback effect, Peltier effect used for temperature measurement, thermocouples, Advantage and limitation of Vapour filled, gas filled, liquid filled, mercury in glass, Bimetallic, Pressure spring thermometer, pyrometers, thermistors, IC based metering, Low temperature and high temperature measurement schemes.

Level Measurements: Importance, advantage and limitation of different instruments, visual level indicators, float type, Purge method of measuring level, Buoyancy method, Resistance and capacitance probes for level measurement, limit switches, level measurement in pressurized vessels, solid level measurement techniques, modern techniques for level measurements and their applications.

UNIT-II

Pressure Measurements: Principle of measurement of absolute/gauge/vacuum, Different type of manometers, Pressure switches, Pirani gauge.

Flow Measurements: Mechanical flow meter, Interferential type, Rotating vane, propeller type, orifice plate, venturi tube, flow nozzle, pivot tube, variable area flow meters, rotameters, Electromagnetic and ultrasonic flow meters, mass flow meters, and turbine flow meters, selection of flow meters and typical application scheme for very low flow and highly viscous fluid.


UNIT-III


Displacement, Linear Velocity Measurement: Gauge blocks, surface plates, use of comparators, optical methods, displacement transducer and typical applications.

UNIT-IV

Moisture and Humidity Measurement: Wet analysis and Dry analysis based methods, Principle Moisture sensing devices- electrical conductivity/capacitance methods, impedance sensors/radio frequency/microwave/Infrared absorption meters, vibrating quartz crystal moisture sensors, principle of operational instrument for measurement of humidity, modern techniques for measurement of humidity.

Vibration and Noise Measurements: Importance and harmful effects, limiting/permissible value under various types of industrial environments, modern measurement techniques.

Text Books:

Reference Books:
MICROPROCESSORS AND MICROCONTROLLERS

Paper Code: ETEC-305
Paper: Microprocessors and Microcontrollers
L T/P C
3 1 4

INSTRUCTIONS TO PAPER SETTERS:

MAXIMUM MARKS: 75

1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.
2. Apart from Question No. 1, rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks.

Objective: The objective of the paper is to facilitate the student with the knowledge of microprocessor systems and microcontroller.

UNIT- I

[1] [No. of hrs. 10]

UNIT- II
8086 Microprocessor: 8086 Architecture, difference between 8085 and 8086 architecture, generation of physical address, PIN diagram of 8086, Minimum Mode and Maximum mode, Bus cycle, Memory Organization, Memory Interfacing, Addressing Modes, Assembler Directives, Instruction set of 8086, Assembly Language Programming, Hardware and Software Interrupts.

[2] [No. of hrs. :12]

UNIT- III
Interfacing of 8086 with 8255, 8254/ 8253, 8251, 8259: Introduction, Generation of I/O Ports, Programmable Peripheral Interface (PPI)-Intel 8255, Sample-and-Hold Circuit and Multiplexer, Keyboard and Display Interface, Keyboard and Display Controller (8279), Programmable Interval timers (Intel 8253/8254), USART (8251), DAC, ADC, LCD, Stepper Motor.

[3] [No. of hrs. :12]

UNIT- IV
Overview of Microcontroller 8051: Introduction to 8051 Micro-controller, Architecture, Memory organization, Special function registers, Port Operation, Memory Interfacing. I/O Interfacing, Programming 8051 resources, interrupts, Programmer’s model of 8051, Operand types, Operand addressing, Data transfer instructions, Arithmetic instructions, Logic instructions, Control transfer instructions, Timer & Counter Programming, Interrupt Programming.

[4] [No. of hrs. 11]

Text Books:
[T3] Ramesh Gaonkar, “MicroProcessor Architecture, Programming and Applications with the 8085”, PHI

References Books:

Scheme and Syllabi for B. Tech-ICE, 1st year (Common to all branches) w.e.f batch 2014-15 and (2nd, 3rd & 4th years) w.e.f batch 2013-14 approved in the 22nd BOS of USET on 30th June, 2014 and approved in the 37th AC Sub Committee Meeting held on 10th July, 2014.
DIGITAL SYSTEM DESIGN

Paper Code: ETEC-309
Paper: Digital System Design

<table>
<thead>
<tr>
<th>L</th>
<th>T/P</th>
<th>C</th>
</tr>
</thead>
<tbody>
<tr>
<td>3</td>
<td>1</td>
<td>4</td>
</tr>
</tbody>
</table>

INSTRUCTIONS TO PAPER SETTERS:

MAXIMUM MARKS: 75

1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.

2. Apart from Question No. 1, rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks.

Objective: To enhance the knowledge and skill of the students in digital system design with emphasis on Hardware Description Language (VHDL HDL).

UNIT I
Introduction to VHDL, design units, data objects, signal drivers, inertial and transport delays, delta delay, VHDL data types, concurrent and sequential statements. Subprograms – Functions, Procedures, attributes, generio, generate, package, IEEE standard logic library, file I/O, test bench, component declaration, instantiation, configuration.

UNIT II
Combinational logic circuit design and VHDL implementation of following circuits – first adder, subtractor, decoder, encoder, multiplexer, ALU, barrel shifter, 4X4 key board encoder, multiplier, divider, Hamming code encoder and correction circuits.

UNIT III
Synchronous sequential circuits design – finite state machines, Mealy and Moore, state assignments, design and VHDL implementation of FSMs, Linear feedback shift register (Pseudorandom and CRC).

UNIT IV
Asynchronous sequential circuit design – primitive flow table, concept of race, critical race and hazards, design issues like metastability, synchronizers, clock skew and timing considerations. Introduction to place & route process, Introduction to ROM, PLA, PAL, Architecture of CPLD (Xilinx/Altera).

Text Books:

Reference Books:
OBJECT ORIENTED PROGRAMMING USING JAVA

Paper Code: ETIC-309          L  T/P  C
Paper: Object Oriented Programming using JAVA          3  0  3

INSTRUCTIONS TO PAPER SETTERS:

1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.
2. Apart from Question No. 1, rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks.

OBJECTIVE: This course introduces the fundamental programming concepts and techniques in Java and is intended for all who plan to use computer programming in their studies and careers.

UNIT I
Java fundamentals: Features of Java, OOPs concepts, Java virtual machine, Reflection byte codes, Byte code interpretation, Data types, variable, arrays, expressions, operators, and control structures.
Introducing java classes: Abstract classes, Static classes, Inner classes, Packages, Wrapper classes, Interfaces, this, super, Access control objects and methods: defining a class, adding variables and methods, creating objects, constructors, class inheritance.
Arrays and String: Creating an array, one and two dimensional arrays, string array and methods, Classes: String and String Buffer classes.

UNIT II
Exception handling: Exception as objects, Exception hierarchy, uncaught exceptions, built in exception, creating your own exceptions, Try, final, Throw, throws.
IO package: Input streams, Output streams, Object serialization, Deserialization, Sample programs on IO files, Filter and pipe streams.
Multi threading: Thread Life cycle, Multi threading advantages and issues, Simple thread program, Java thread model: priorities, synchronization, messaging, thread classes, Runnable interface, and inter thread Communication, suspending, resuming and stopping threads.

UNIT III
GUI: Introduction to AWT programming, Layout and component managers and menus, handling Image, animation, sound and video.
Event handling: Different Mechanism, the Delegation Event Model, Event Classes, Event Listener Interfaces.
Applet class: Applet life-cycle, passing parameters embedding in HTML, Swing components – JApplet, JButton, JFrame, etc., Sample swing programs, servlets.

UNIT IV
Networking: Basics, networking classes and interfaces, using java.net package, doing TCP/IP and Data-gram Programming.
Database Connectivity: JDBC architecture, connectivity and working with connection interface, Working with statements, Creating and executing SQL statements, working with Result Set.

Text Books:
[T1] Patrick Naughton and Herbert Schildt, “Java-2 The Complete Reference”, 1999, TMH

Reference Books:
INDUSTRIAL MANAGEMENT

Paper Code: ETMS-311
Paper: Industrial Management

<table>
<thead>
<tr>
<th>L</th>
<th>T/P</th>
<th>C</th>
</tr>
</thead>
<tbody>
<tr>
<td>3</td>
<td>0</td>
<td>3</td>
</tr>
</tbody>
</table>

INSTRUCTIONS TO PAPER SETTERS:

MAXIMUM MARKS: 75

1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.

2. Apart from Question No. 1, rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks.

Objective: The course provides a broad introduction to some aspects of business management and running of business organization.

UNIT I
Industrial relations- Definition and main aspects. Industrial disputes and strikes. Collective bargaining.

UNIT II

UNIT III

UNIT IV

Text Books:

Reference Books:
COMMUNICATION SKILLS FOR PROFESSIONALS LAB

Paper Code: ETHS-351                                                                                   L T/P C
Paper: Communication Skills for Professionals Lab                                                   0  2  1

Objective: To develop communication competence in prospective engineers so that they are able to communicate information as well as their thoughts and ideas with clarity and precision. These activities will enhance students' communication skills with a focus on improving their oral communication both in formal and informal situations. They will develop confidence in facing interviews and participating in group discussions which have become an integral part of placement procedures of most business organisations today.

Lab Activities to be conducted:

1. Listening and Comprehension Activities – Listening to selected lectures, seminars, news (BBC, CNN, etc.). Writing a brief summary or answering questions on the material listened to.
2. Reading Activities – Reading different types of texts for different purposes with focus on the sound structure and intonation patterns of English. Emphasis on correct pronunciation.
3. Conversation Activities – Effective Conversation Skills; Formal/Informal Conversation; Addressing higher officials, colleagues, subordinates, a public gathering; Participating in a video conference.
4. Making an Oral Presentation – Planning and preparing a model presentation; Organizing the presentation to suit the audience and context; Connecting with the audience during presentation; Projecting a positive image while speaking; Emphasis on effective body language.
5. Making a Power Point Presentation – Structure and format; Covering elements of an effective presentation; Body language dynamics.
6. Making a Speech – Basics of public speaking; Preparing for a speech; Features of a good speech; Speaking with a microphone. Famous speeches may be played as model speeches for learning the art of public speaking. Some suggested speeches: Barack Obama, John F Kennedy, Nelson Mandela, Mahatma Gandhi, Jawahar Lal Nehru, Atal Bihari Vajpayee, Subhash Chandra Bose, Winston Churchill, Martin Luther King Jr.
7. Participating in a Group Discussion – Structure and dynamics of a GD; Techniques of effective participation in group discussion; Preparing for group discussion; Accepting others' views / ideas; Arguing against others' views or ideas, etc.
8. Participating in Mock Interviews – Job Interviews: purpose and process; How to prepare for an interview; Language and style to be used in an interview; Types of interview questions and how to answer them.

Suggested Lab Activities:

1. Interview through telephone/video-conferencing
2. Extempore, Story Telling, Poetry Recitation
3. Mock Situations and Role Play; Enacting a short skit
4. Debate (Developing an Argument), News Reading and Anchoring.

Reference Books:


Note: The Communication Skills Lab should be equipped with computers, microphones, an internet connection, overhead projector, screen, sound system, audio/video recording facilities, and seating arrangement for GDs and mock interviews. The student activities may be recorded and students may replay them to analyse and improve their pronunciation, tone, expressions, body language, etc.
Traditional language lab softwares are not mandatory and may be used by students to practice and enhance their language competence. Such softwares are usually elementary in nature and are mostly based on British/American English (pronunciation, accent and expression). They should preferably be in Indian English.
INDUSTRIAL INSTRUMENTATION LAB

Paper Code: ETIC-353
Paper: Industrial Instrumentation Lab

<table>
<thead>
<tr>
<th>L</th>
<th>T/P</th>
<th>C</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>2</td>
<td>1</td>
</tr>
</tbody>
</table>

List of Experiments:

4. Study and operation of a typical Pneumatic/Hydraulic control scheme.
6. Introduction and application of LabVIEW for Industrial applications.
7. Measurement of Temperature (Thermistor/RTD/Thermocouple) using LabVIEW.
8. Measurement of speed using LabVIEW.
9. Measurement of Level using LabVIEW.
10. Measurement of flow (turbine flow meter) using LabVIEW.
11. Measurement of Pressure using LabVIEW.

NOTE:- At least 8 Experiments out of the list must be done in the semester.
MICROPROCESSORS AND MICROCONTROLLERS LAB

Paper Code: ETEC-355
Paper: Microprocessors and Microcontrollers Lab

List of Experiments:

1. Write a program to add and subtract two 16-bit numbers with/without carry using 8086.
2. Write a program to multiply two 8-bit numbers by repetitive addition method using 8086.
3. Write a Program to generate Fibonacci series.
4. Write a Program to generate Factorial of a number.
5. Write a Program to read 16-bit Data from a port and display the same in another port.
6. Write a Program to generate a square wave using 8254.
7. Write a Program to generate a square wave of 10 kHz using Timer 1 in mode 1 (using 8051).
8. Write a Program to transfer data from external ROM to internal (using 8051).
9. Design a Minor project using 8086 Microprocessor (Ex: Traffic light controller/temperature controller etc)
10. Design a Minor project using 8051 Microcontroller

NOTE: - At least 8 Experiments out of the list must be done in the semester.
DIGITAL SYSTEM DESIGN LAB

Paper Code: ETEC-351
Paper: Digital System Design Lab

List of Experiments:

1. Design all gates using VHDL.
2. Write VHDL programs for the following circuits, check the wave forms and the hardware generated
   i) half adder
   ii) full adder
3. Write VHDL programs for the following circuits, check the wave forms and the hardware generated
   i) multiplexer
   ii) demultiplexer
4. Write VHDL programs for the following circuits, check the wave forms and the hardware generated
   i) decoder
   ii) encoder
5. Write a VHDL program for a comparator and check the wave forms and the hardware generated
6. Write a VHDL program for a code converter and check the wave forms and the hardware generated
7. Write a VHDL program for a FLIP-FLOP and check the wave forms and the hardware generated
8. Write a VHDL program for a counter and check the wave forms and the hardware generated
9. Write VHDL programs for the following circuits, check the wave forms and the hardware generated
   i) ALU
   ii) shift register

NOTE: - At least 8 Experiments out of the list must be done in the semester.
OBJECT ORIENTED PROGRAMMING USING JAVA LAB

Paper Code: ETIC-357
Paper: Object Oriented Programming Using Java Lab

List of Experiments:

1. Create a java program to implement stack and queue concept.
2. Write a java package to show dynamic polymorphism and interfaces.
3. Write a java program to show multithreaded producer and consumer application.
4. Create a customized exception and also make use of all the 5 exception keywords.
5. Convert the content of a given file into the uppercase content of the same file.
6. Develop an analog clock using applet.
7. Develop a scientific calculator using swings.
8. Create an editor like MS-word using swings.
9. Create a servlet that uses Cookies to store the number of times a user has visited your servlet.
10. Create a simple java bean having bound and constrained properties.

NOTE:- At least 8 Experiments out of the list must be done in the semester.
INSTRUCTIONS TO PAPER SETTERS:  

1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.

2. Apart from question no. 1, rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks.

Objective: The objective of the paper is to facilitate the students with the working and applications of a large class of pneumatic and hydraulic instruments used in various plants and industries.

UNIT - I
Introduction: Basic requirement for Pneumatic System, Servicing compressed air: Air compressors, air treatment stages, pressure regulation (FRL unit) Introduction to hydraulic system, comparison of pneumatic & hydraulic system.  

UNIT - II
Pneumatic & hydraulic Actuators, cylinders valve positioner, piston & motor actuators, electro pneumatic actuators, cylinder lubrication, cylinder with sensors, hydraulic actuators, control valves types of control valves.  

UNIT - III
Basic pneumatic circuits, Timing & sequence diagram: Cylinder sequencing hydraulic & pneumatic Accessories pneumatic telemetry systems: Pneumatic temperature & pressure transmitters their working and applications, electrical control in pneumatic circuit.  

UNIT - IV

Text Books:

Reference Books:
PROCESS CONTROL

Paper Code: ETIC–304

Paper: Process Control

L T/P C
3 1 4

INSTRUCTIONS TO PAPER SETTERS:

MAXIMUM MARKS: 75

1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.
2. Apart from question no. 1, rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks.

Objective: To give students comprehension about the architectures, mechanisms and algorithms for maintaining the output of a specific process within a desired range. To give students knowledge about various tuning methods of feedback controllers and multiloop controllers.

UNIT - I

Design Aspects of a process control system: Design Elements of a control system. Control aspects of a complete chemical plant. Development of a Mathematical model. Modelling considerations for control purposes. Dynamic Behavior of First order system, second order system and higher order systems. Introduction to Feedback control. Dynamic Behaviour of feedback-controlled processes. Stability Analysis of feedback systems [T1, T2][No. of Hours 11]

UNIT - II


UNIT - III

Study of multiple loops controller: Cascade Control System. Selective control system. Split Range Control. Feed forward and Ratio control. Adaptive and Inferential control systems. [T1, T2] [No. Of Hours 11]

UNIT – IV

Interaction & De-coupling of control loop: Interaction of control loops. relative gain array and selection of the loops. Design of non-interaction control loop. Multivariable model. Predictive control. Simple and multivariable dynamic matrix control loop. [T1, T2][No. of Hours 11]

Text Books:

Reference Books:
[R2] D.R. Coughanour,” Process system and analysis and control”, TMH

Scheme and Syllabi for B. Tech-ICE, 1st year (Common to all branches) w.e.f batch 2014-15 and (2nd, 3rd & 4th years) w.e.f batch 2013-14 approved in the 22nd BOS of USET on 30th June, 2014 and approved in the 37th AC Sub Committee Meeting held on 10th July, 2014.
DIGITAL SIGNAL PROCESSING

Paper Code: ETEC-306

Paper: Digital Signal Processing

L T/P C
3 1 4

INSTRUCTIONS TO PAPER SETTER:

MAXIMUM MARKS: 75

1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.
2. Apart from Question No. 1, rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be 12.5 marks.

Objectives: The aim of this course is to provide in depth knowledge of various digital signal processing techniques and design of digital filters, learn the concept of DFT FFT algorithms, and design of digital filters using different approximations, DSP processor and architecture. The prerequisites of this subject are basic knowledge of signal and systems.

UNIT–I

Frequency Domain Sampling: The Discrete Fourier Transform, Properties of the DFT, Linear filtering methods based of the DFT. [T1]

Efficient computation of the DFT: Principal Of FFT, Fast Fourier Transform Algorithms, Applications of FFT Algorithms. A linear filtering approach to computation of the DFT.

Application of DFT, Design of Notch filter [T2,T1][No. of Hours: 11]

UNIT–II

Design & Structure of IIR filters from Analog filters: Impulse Invariance; Bilinear transformation and its use in design of Butterworth and Chebyshev IIR Filters; Frequency transformation in Digital Domain, Direct, Cascade, Parallel & transposed structure [T1]

Design & structure of FIR filters: Symmetric and anti-symmetric FIR filters; Design of Linear Phase FIR filters using windows, Frequency Sampling Method of FIR design, Direct, Cascade, Frequency Sampling, transposed structure

[T2][No. of Hours: 11]

UNIT–III

Implementation of Discrete Time Systems:
Lattice structures, Lattice and Lattice-Ladder Structures, Schur - Cohn stability Test for IIR filters; Discrete Hilbert Transform.

Linear predictive Coding:
Lattice filter design, Levension Darwin Technique, Schur Algorithm

[T1,T2][No. of Hours: 10]

UNIT–IV

Quantization Errors In Digital Signal Processing: Representation of numbers, Quantization of filter coefficients, Round-off Effects in digital filters.

Multirate Digital Signal Processing: Decimation, Interpolation, Sampling rate conversion by a rational factor; Frequency domain characterization of Interpolator and Decimator; Polyphase decomposition.

[T2][No. of Hours: 10]

Text Books:
[T2] Proakis and Manolakis, Digital Signal Processing, PHI Publication

Reference Books:
ANALYTICAL INSTRUMENTATION

Paper Code: ETIC-308
Paper: Analytical Instrumentation
L  T/P  C
3  1  4

INSTRUCTIONS TO PAPER SETTERS:

MAXIMUM MARKS: 75
1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.
2. Apart from question no. 1, rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks.

Objective: The objective of the paper is to facilitate the students with a large class of instruments used to analyze materials and to establish the composition.

UNIT- I
Fundamentals of Analytical Instruments: Elements of an analytical system, signal conditioning in analytical, performance requirements of analytical instruments; validation
Gas Chromatography: Theory of Chromatography, Construction and working of gas chromatography, gas-solid chromatography, Liquid Chromatography: types of liquid chromatography, high pressure liquid chromatography.

[T1, T2] [No. of Hrs. 11]

UNIT- II
Mass Spectrometer: Introduction, types of mass spectrometers, components of mass spectrometers, Resolution, applications; gas chromatograph-mass spectrometer (GC-MS), liquid chromatograph-mass spectrometer.
Spectrometer: Raman spectrometer, photoacoustic and photothermal spectrometer, NMR spectrometer, ESR Spectrometer, Electron and ion spectrometer, X-ray spectrometer

[T1, T2] [No. of Hrs. 11]

UNIT- III
Calorimeters and Spectrophotometers: Visible-Ultraviolet spectrophotometers, infrared photometers, Atomic absorption spectrophotometers, flame photometers.


[T1, T2] [No. of Hrs. 11]

UNIT- IV:
Environmental Pollution Monitoring Instruments: Air pollution monitoring instruments: Carbon Monoxide, Sulphur Dioxide, Nitrogen Oxides, Hydrocarbons, Water pollution monitoring instruments.

[T1, T2] [No. of Hrs. 11]

Text Books:

Reference Books:
MODERN CONTROL SYSTEMS

Paper Code: ETIC-310  L   T/P   C
Paper: Modern Control Systems  3   1   4

INSTRUCTIONS TO PAPER SETTERS: MAXIMUM MARKS-75
1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have
objective or short answer type questions. It should be of 25 marks.
2. Apart from Q. No.1 rest of the paper shall consist of four units as per the syllabus, every unit should
have two questions. However, student may be asked to attempt only 1 question from each unit. Each
question should be 12.5 marks.

Objective: To impart knowledge of state space, discrete systems, non-linear systems and adaptive control.

UNIT–I
State Space Analysis
Introduction, state space representation of continuous LTI systems, transfer function and state variables, transfer
matrix, EIGEN values and EIGEN vectors, Solution of State equations, controllability and observability,
canonical forms (CCF, OCF, DCF, JCF).

UNIT–II
Discrete System
Introduction to discrete time systems, sampling process, Z-transform and inverse Z-transforms and hold circuits,
presentation by difference equation and its solution, pulse transfer function, transient and steady state responses,
Dead beat response, steady state error, Representation of discrete systems in state variable form and its solution,
stability of digital control system, digital equivalent of conventional controller/compensator.

UNIT – III
Non-Linear System
Introduction, Non-linear system behaviour and different types of non-linearities, Describing function analysis,
assumptions and definitions, DF of common non-linearities, Phase Plane Analysis, singular points, construction
of phase portrait, phase plane analysis of linear/non-linear systems, existence of limit cycles, jump phenomenon,
stability analysis.

UNIT – IV
Lyapunov Theory and Adaptive Control
Lyapunov direct method, positive definite functions and Lyapunov functions, existence of Lyapunov functions,
Lyapunov analysis of LTI systems, variable gradient method, Krasvoskii method, performance analysis, Popov's
stability criteria.
Introduction to basic approaches to adaptive control - Model reference adaptive control systems, self tuning
regulators, Applications of adaptive control.

Text Books:

Reference Books:
[R2] Brian D.O. Adnerson & John B. Moore, Optimal Control
[R4] Shastri & Badson, Adaptive Control, PHI
DATA COMMUNICATION AND NETWORKS

Paper Code: ETEC-310  
Paper: Data Communication and Networks  
L T/P C  3 1 4

INSTRUCTIONS TO PAPER SETTERS:  

MAXIMUM MARKS: 75

1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.
2. Apart from question no. 1, rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks

Objective: To understand the basics of data communication and networking protocols.

UNIT I

[No of Hours: 10]

UNIT II
Error detection and correction: Types of errors detection, Vertical Redundancy Check (VRC), Longitudinal Redundancy Check (LRC), Cyclic Redundancy Check (CRC), Check sum, Error correction Mechanism, Data link control: Line discipline, Flow control, Error control. Data link protocols: Asynchronous protocols, Synchronous protocols, Character oriented protocols, Bit oriented protocols, Link access procedures, HDLC.

[No of Hours: 12]

UNIT III

[No of Hours: 12]

UNIT IV
Networking and internetworking devices: Repeaters, Bridges, Gateways and Other devices, Unicast and Multicast Routing, Virtual LANs. Network layers: Addressing (IPV4, IPV6), Subnetting, Superinetting. Internetworking - Other protocols and network layers. TCP/ IP protocol suite: Overview of TCP/IP.

[No of Hours: 10]

Text Books:

Reference Books:
[R2] Introduction to Data communications and Networking, Tomasi, Pearson Education

Scheme and Syllabi for B. Tech-ICE, 1st year (Common to all branches) w.e.f batch 2014-15 and (2nd, 3rd & 4th years) w.e.f batch 2013-14 approved in the 22nd BOS of USET on 30th June, 2014 and approved in the 37th AC Sub Committee Meeting held on 10th July, 2014.
PNEUMATIC AND HYDRAULIC INSTRUMENTATION LAB

Paper Code: ETIC-352  L  T/P  C
Paper: Pneumatic and Hydraulic Instrumentation Lab  0  2  1

List of Experiments:

1. Operating single acting cylinder and double acting cylinder using 3/2 push button valve.
2. Operating double acting cylinder using 5/2 pilot operated valve.
4. Operating single and double acting cylinder using special purpose valve – Time delay valve, Quick exhaust
   i)   valve, Twin pressure valve, Check valve etc.
5. Write a program for sequencing of two cylinders using pneumatic components only.
6. Write a program for sequencing of two cylinders using electro pneumatic components.
7. Sequencing of multiple double acting piston cylinder arrangement using electro-pneumatic components.
8. PLC programming- Operate single acting cylinder and double acting cylinder using push button and direction
   i)   Control valve. Use push buttons in the AND, OR and Latching conditions.
9. Write a PLC program for to and fro motion of single acting cylinder and double acting cylinder automatically.
10. Write a PLC program for sequencing of three cylinders in following sequence
    1. A+B+C-A+B+C+ A- B+C+ A+B+C+
    2. A+ B-C- A+B+C- A+B+C+
    3. A+B+C- A+B-C- A- B-C-

NOTE:- At least 8 Experiments out of the list must be done in the semester.
PROCESS CONTROL LAB

Paper Code: ETIC-354
Paper: Process Control Lab

List of Experiments:

1. To implement SILO Control using PLC.
2. To control the PH of reaction Vessel using PLC.
3. To Control the operation of Bottling plant using PLC.
4. To control a chemical process using PLC.
5. To control the operation of Washing Machine using PLC.
6. To implement cascade control using Process Control Trainer.
7. To study the operation of PID Control using Process control Trainer.
8. To implement feed forward control using Process Control Trainer.
10. To study the characteristics of Control Valve using Process Control Trainer.

NOTE:- At least 8 Experiments out of the list must be done in the semester.
DIGITAL SIGNAL PROCESSING LAB

Paper Code: ETEC-356
Paper: Digital Signal Processing Lab

List of Experiments:

Software Experiments:
1. Generation of basic signals sine, cosine, ramp, step, impulse and exponential in continuous and discrete domains using user defined functions.
2. Write a MATLAB program to find convolution (linear/circular) and correlation of two discrete signals.
3. Perform linear convolution using circular convolution and vice versa.
4. Write a MATLAB program to
   a. Find 8 point DFT, its magnitude and phase plot and inverse DFT.
   b. Find 16 point DFT, its magnitude and phase plot and inverse DFT.
5. Perform the following properties of DFT:
   a. Circular shift of a sequence.
   b. Circular fold of a sequence.
6. Write a MATLAB Program to design FIR Low pass filter using
   a. Rectangular window
   b. Hanning window
   c. Hamming window
   d. Bartlett window
7. Write a MATLAB program to
   b. Implement a Low pass / High pass / Band pass / Band stop IIR Filter using Chebyshev Approximation.

Hardware Experiments using Texas Instruments Kits-DSK 6713:
8. Introduction to Code composer Studio.
9. Write a program to generate a sine wave and see the output on CRO
10. Write a Program to Generate ECHO to give audio file.
11. Write a program to demonstrate Band Stop filter by FIR.

Additional Experiments:
12. Write a program to generate a cos wave and see the output on CRO
13. Write a program to blink the LED
14. Write a program to display a string on LCD.

NOTE:- At least 8 Experiments out of the list must be done in the semester.
MODERN CONTROL SYSTEMS LAB

Paper Code: ETIC-358
Paper: Modern Control Systems Lab

<table>
<thead>
<tr>
<th>L</th>
<th>T/P</th>
<th>C</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>2</td>
<td>1</td>
</tr>
</tbody>
</table>

List of Experiments:

1. Study of open loop and closed loop time/frequency responses of first/second order LTI system
2. Conversion of transfer functions to state model of LTI system and vice versa
3. Determine State Space Model of a given system and determine its controllability and observability.
4. Analysis of Zero order hold and first order hold circuits.
5. Conversion of transfer functions to state model of discrete time system.
6. To determine state transition matrix of a given system.
7. Study of saturation and dead zone non-linearity using describing function technique of a relay control system.
8. To draw phase trajectory of a given non-linear system.
9. Experiments based on PLC applications e.g. Lift control models, pick and place module etc.
10. Study of operation of a stepper motor interface with microprocessor.

NOTE:- At least 8 Experiments out of the list must be done in the semester.
**DATA COMMUNICATION AND NETWORKS LAB**

<table>
<thead>
<tr>
<th>Paper Code: ETEC-358</th>
<th>L</th>
<th>T/P</th>
<th>C</th>
</tr>
</thead>
<tbody>
<tr>
<td>Paper: Data Communication and Networks Lab</td>
<td>0</td>
<td>2</td>
<td>1</td>
</tr>
</tbody>
</table>

**List of Experiments:**

1. Introduction to Computer Network laboratory  
   Introduction to Discrete Event Simulation  
   Discrete Event Simulation Tools - ns2/ns3, Omnet++
2. Using Free Open Source Software tools for network simulation – I  
   Preliminary usage of the tool ns3  
   Simulate telnet and ftp between N sources - N sinks (N = 1, 2, 3). Evaluate the effect of increasing data rate on congestion.
3. Using Free Open Source Software tools for network simulation - II  
   Advanced usage of the tool ns3  
   Simulating the effect of queueing disciplines on network performance - Random Early Detection/Weighted RED / Adaptive RED (This can be used as a lead up to DiffServ / IntServ later).
4. Using Free Open Source Software tools for network simulation - III  
   Advanced usage of the tool ns3  
   Simulate http, ftp and DBMS access in networks
5. Using Free Open Source Software tools for network simulation - IV  
   Advanced usage of the tool ns3  
   Effect of VLAN on network performance - multiple VLANs and single router.
6. Using Free Open Source Software tools for network simulation - IV  
   Advanced usage of the tool ns3  
   Effect of VLAN on network performance - multiple VLANs with separate multiple routers.
7. Using Free Open Source Software tools for network simulation - V  
   Advanced usage of the tool ns3  
   Simulating the effect of DiffServ / IntServ in routers on throughput enhancement.
8. Using Free Open Source Software tools for network simulation - VI  
   Advanced usage of the tool ns3  
   Simulating the performance of wireless networks
9. Case Study I: Evaluating the effect of Network Components on Network Performance  
   To Design and Implement LAN With Various Topologies and To Evaluate Network Performance Parameters for DBMS etc
10. Case Study II: Evaluating the effect of Network Components on Network Performance  
    To Design and Implement LAN Using Switch/Hub/Router As Interconnecting Devices For Two Different LANs and To Evaluate Network Performance Parameters.
11. Mini project - one experiment to be styled as a project of duration 1 month (the last month)

**NOTE:** At least 8 Experiments out of the list must be done in the semester.
DIGITAL CONTROL SYSTEMS

Paper Code: ETIC–401                                  L  T/P C
Paper: Digital Control Systems                        3  1  4

INSTRUCTIONS TO PAPER SETTERS:                          MAXIMUM MARKS: 75
1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.
2. Apart from Question No. 1, rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks.

Objective: This involves students in the construction of a control theory for discrete-time models. To give comprehension about the Z transforms and design of discrete time control systems with different methods of stability in Z domain.

UNIT - I
Introduction to Discrete Time Control Systems

[T1, T2][No. of Hours: 12]

UNIT - II
Z plane Analysis of Discrete-Time control systems:
Introduction, Impulse sampling and Data Hold, Signal Flow Graph and Matrix Representation of Sampled Data System: Block Diagram Analysis and Transfer Functions of Closed Loop Sampled Data Systems, Signal Flow Graphs of Sampled Data Systems, The pulse transfer function.

[T1, T2][No. of Hours: 10]

UNIT - III
Design of Discrete –Time control system by Conventional Methods:

[T1, T2][No. of Hours: 11]

UNIT – IV
Digital Controller Design:
Introduction to controller design, Control system specification, Compensation, phase lag compensator, phase lead compensator, phase lead design procedure, lag lead compensator, PID controllers, Analysis and design of Digital Control Systems using root locus and transform techniques.

[T1, T2][No. of Hours: 11]

Text Books:

Reference Books:

Scheme and Syllabi for B. Tech-ICE, 1st year (Common to all branches) w.e.f batch 2014-15 and (2nd, 3rd & 4th years) w.e.f batch 2013-14 approved in the 22nd BOS of USET on 30th June, 2014 and approved in the 37th AC Sub Committee Meeting held on 10th July, 2014.
BIOMEDICAL INSTRUMENTATION

Paper Code: ETIC-403
Paper: Biomedical Instrumentation

L T/P C
3 0 3

INSTRUCTIONS TO PAPER SETTERS:

1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.
2. Apart from Q. No. 1 rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks.

MAXIMUM MARKS: 75

Objective:-The objective of teaching this subject is to make students understand the applications of electronics in diagnostic and therapeutic area. Further the methods of recording various bio potentials; measurement of biochemical and physiological information are explained. The topics such as Patient Monitoring systems, Audiometers, imaging systems, Patients safety are also included. The emerging Computer Applications in Biomedical field are also dealt with.

UNIT I

T1, T2][No of Hours:-11]

UNIT II

T1, T2][No of Hours:-11]

UNIT III
Modern Imaging systems: Introduction, Basic principle & Block diagram of x-ray machine, x-ray Computed Tomography (CT), Magnetic resonance imaging system (NMR), ultrasonic imaging system. Eco-Cardiograph, Eco Encephalography, Ophthalmic scans, MRI. Therapeutic Equipments: Cardiac pacemakers, cardiac defibrillators, Hemodialysis machine, Surgical diathermy machine.

T1, T2][No of Hours:-11]

UNIT III

T1, T2][No of Hours:-11]

Text Books:

Reference Books:
[R3] Leslie Cromwell, Fred J. Weibell & Erich A. Pfeiffer, “Biomedical Instrumentation & Measurements”, PHI

Scheme and Syllabi for B. Tech-ICE, 1st year (Common to all branches) w.e.f batch 2014-15 and (2nd, 3rd & 4th years) w.e.f batch 2013-14 approved in the 22nd BOS of USET on 30th June, 2014 and approved in the 37th AC Sub Committee Meeting held on 10th July, 2014.
ARTIFICIAL NEURAL NETWORKS

**Paper Code:** ETIC–405

<table>
<thead>
<tr>
<th>Paper: Artificial Neural Networks</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>L</strong></td>
</tr>
<tr>
<td>3</td>
</tr>
</tbody>
</table>

**INSTRUCTIONS TO PAPER SETTERS:**

**MAXIMUM MARKS:** 75

1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.

2. Apart from question no. 1, rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks.

**Objective:** To give students knowledge about modeling biological neuron networks. To provide students with the neural networks ability to perform computations based on the flexibility and power of human brain by artificial means.

**UNIT - I**

**Introduction:**
Neural networks characteristics, History of development in neural networks principles, Artificial neural terminology, Model of a neuron, Topology, Neural Networks: biological concepts, neuron model, transfer functions, feed forward, feedback, supervised and unsupervised models, Learning methods- Hebb’s, delta, back-propagation and competitive learning.

**[T1, T2] [No. of Hours: 11]**

**UNIT - II**

**Learning Methods & Neural Network Models:**

**[T1, T2] [No. of Hours: 12]**

**UNIT - III**

**Artificial Neural Networks:**

**[T1, T2] [No. of Hours: 11]**

**UNIT – IV**

**Applications of neural networks:**
Applications such as pattern recognition, Pattern mapping, Associative memories, Optimization, Speech and decision-making, VLSI implementation of neural networks.

**[T1, T2] [No. of Hours: 10]**

**Text Books:**

| [T1] | B. Yegnanarayana, “Artificial Neural Networks”, PHI. |

**Reference Books:**

INDUSTRIAL AUTOMATION AND CONTROL

Paper Code: ETIC-407
Paper: Industrial Automation and Control

<table>
<thead>
<tr>
<th>L</th>
<th>T/P</th>
<th>C</th>
</tr>
</thead>
<tbody>
<tr>
<td>3</td>
<td>0</td>
<td>3</td>
</tr>
</tbody>
</table>

**INSTRUCTIONS TO PAPER SETTERS:**

1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.
2. Apart from question no. 1, rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks.

**Maximum Marks: 75**

Objective: The objective of the paper is to facilitate the students with a large class of industrial processes and methods to include automation in them.

**UNIT - I**
Introduction and Architecture of Industrial Automation & Control Systems; Control strategies: Feed-forward and Ratio Control, Predictive Control, Control of Systems with Inverse Response, Special Control Structures: Cascade, Override and Split Range Control.

**UNIT - II**
PLC: Introduction to Sequence/Logic Control and Programmable Logic Controllers; Software Environment and Programming of PLCs, Formal Modelling of Sequence Control Specifications and Structured RLL Programming, Programming of PLCs— Sequential Function Charts; The PLC Hardware Environment.

**UNIT - III**
Introduction to Computer Numerically Controlled (CNC) Machines; Interpolation, Control and Drive, Control Valves, Electric, Pneumatic and Hydraulic Actuators, Analog and Digital I/O system, Signal conditioning components; DAQ, Data acquisition software overview; DAQ cards and their applications.

**UNIT - IV**
Higher Levels of Automation Systems: Supervisory Control; Distributed Digital Control Systems (DCS), Direct digital control (DDC), SCADA— Introduction; field data interface devices, communication network and other details, System Architecture— monolithic, distributed, networked, SCADA protocols in short; Significance; application of SCADA in industry; installation of SCADA Systems; security and weakness of SCADA Systems.

**Text Books:**

[T1, T2][No. of Hrs. 11]

**Reference Books:**

[R2] Bhaktar, Marshal, “Distributed Computer control & Industrial Automation”, Dekker Publication
Objective: Mechatronics is the combination of mechanical and electronics automation and computers. Nowadays all the mechanical machines have been made computer controlled. The Subject details the basic hardware and software elements used for proper and successful operation of various equipments. The knowledge of this subject will be helpful to students while working in industries.

UNIT - I
Mechanical Actuating Systems: Types of motion, Degrees of freedom, constraints, Kinematic Chains, Cam, Gear and gear trains, Ratchet and pawl Belt drive, chain drive, Bearing, pre loading.
Hydraulic & Pneumatic Actuation Systems: Fluid power systems, hydraulic systems, Pneumatic systems, system structure and signal flow, hydraulic pumps and Pressure Control Valves and regulation, air compressors and treatment, Cylinders, Direction Control Valves, Process control valves, Rotary Actuators, Accumulators, Amplifiers, and Pneumatic Sequencing Problems.

UNIT - II

Digital Electronics and systems:
Number Systems, Binary Mathematics, Boolean Algebra, Gates and Integrated Circuits Like 7408, 7402, Karnaugh Maps, Application of Logic Gates as: Parity Generators, Digital Comparators, BCD to Decimal Decoders, Flip Flops and applications, sequential logic, Microprocessor and microcontrollers, programming, instruction set, assembly language, C programming for Intel 8051 / 8082 micro-controller.

UNIT - III
System Interfacing and data acquisition:
Data acquisition systems, Data Loggers, SCADA, Interfacing requirements, Buffers, Darlington Pair, Handshaking, Serial and Parallel Port Interfacing, Peripheral Interface Adapters, Analog to Digital Conversion, Digital To Analog Conversion, Sample and Hold Amplifiers, Multiplexers, Time Division Multiplexing, Digital Signal Processing, Pulse Modulation, Component Interconnection and Impedance Matching, Interfacing Motor drives. Electrical power supply and protection.

UNIT - IV
Programmable logic controllers:
Programmable logic controllers (PLC) Structure, Input / Output Processing, principles of operation, PLC versus computer, Programming Languages, programming using Ladder Diagrams, Logic Functions, Latching, Sequencing, Timers, Internal Relays And Counters, Shift Registers, Master and Jump Controls, Jumps, Data Movement, Code Conversion, Data handling and manipulation, selecting a PLC.
Case studies: Mechatronic approach to design, Boat Auto pilot, high speed tilting train, automatic car park system, coin counter, engine management system, autonomous mobile system, antilock brake system control, Auto-Focus Camera, Printer, Domestic Washing Machine, Optical Mark Reader, Bar Code Reader and Pick and Place robot Arm, Using PLC for extending and retracting a pneumatic piston and two pneumatic pistons in different combinations, control of vibrating machine, control of process tank, control of conveyor motor, detecting, sorting and packaging unit.

Text Book:

Reference Books:
[R2]  Dan Necsulescu, Mechatronics, Pearson
ELECTRICAL DRIVES

Paper Code: ETEE-401        L  T/P C
Paper: Electrical Drives  3  0  3

INSTRUCTIONS TO PAPER SETTERS: MAXIMUM MARKS: 75
1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.
2. Apart from Q. No. 1 rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks.

Objective: The objective of the paper is to facilitate the student with the basics of Electrical Drives that are required for an engineering student.

UNIT- I
Dynamics of Electric Drives: Types of loads, quadrant diagram of speed time characteristics, Basic and modified characteristics of dc and ac motors, equalization of load, steady state stability, calculation of time and energy loss, control of electric drives, modes of operation, speed control and drive classifications, closed loop control of drives, selection of motor power rating, class of duty, thermal considerations.

UNIT- II
DC Motor Drives: DC motor speed control, Methods of armature control, field weakening, semiconductor controlled drives, starting, braking, transient analysis, controlled rectifier fed dc drives, chopper controlled dc drives.

UNIT- III
Induction Motor Drives: Three phase induction motor starting, braking, transient analysis, speed control from stator and rotor sides, stator voltage control, variable frequency control from voltage sources and current sources, static rotor resistance control, slip power recovery, static Scherbius and static Kramer drive.

UNIT-IV Drives with Special Machine:
Introduction to permanent magnet machines, thermal properties of PM, concept of BLDC motor, 120° and 180° operation, rotor position detection, open loop voltage control, closed loop current control, high speed single pulse operation, permanent magnet synchronous machines, rotor position detection and synchronization, sinusoidal PWM excitation, closed and open loop control, PMSG and its application to wind energy; stepper motor, current and voltage control, drive circuits, SRM drive, modeling and analysis of SRM, different configurations of converters, closed and open loop operation, high speed operation with angle of advance.

Text Books:

References Books:
[R2] Ned Mohan, “Electrical Machines And Drives” Wiley India Publication
[R5] Bimal K Bose, “Power Electronics and Variable Frequency Drives” Wiley India Publication
INSTRUMENTATION DIAGNOSTICS

Paper Code: ETIC-413
Paper: Instrumentation Diagnostics

<table>
<thead>
<tr>
<th>L</th>
<th>T/P</th>
<th>C</th>
</tr>
</thead>
<tbody>
<tr>
<td>3</td>
<td>0</td>
<td>3</td>
</tr>
</tbody>
</table>

INSTRUCTIONS TO PAPER SETTERS:

1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.

2. Apart from Q. No. 1 rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks.

Maximum Marks: 75

Objective: The objective of the paper is to facilitate the students with the techniques to monitor and diagnose the faults in industrial instruments and maintenance strategies for them.

UNIT - I
Introduction to fault model & fault simulation. Fault location & Board level testing, Test generation for combinational circuits. D - Algorithm & its improvement.

UNIT - II

UNIT - III

UNIT - IV

Text Books:

Reference Books:
[R1] Handbook of condition monitoring, B. K. N. Rao, Elsevier
PROCESS MODELLING & OPTIMIZATION TECHNIQUES

Paper Code: ETMT-415
Paper: Process Modelling & Optimization Techniques

INSTRUCTIONS TO PAPER SETTERS:

1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.
2. Apart from Question No. 1, rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks.

Objective: The objective of the paper is to facilitate the students with the field concerned with the modeling and optimization techniques of various industrial processes.

UNIT - I
Process modelling, SISO and MIMO processes, Coupled processes, Study of Biochemical reactors, CSTR, Steam drum level, surge vessel level control, Batch reactor and Biomedical systems.

UNIT - II
Introduction to Control and optimization of Boiler, Cooling Tower, Distillation column, Reactors, Heat exchangers, condenser and evaporator.

UNIT - III
Overview of optimisation techniques, Cost functions, supervised and unsupervised methods, Linear Optimisation–Least squares method, Recursive Least square, subset selection.

UNIT - IV

Text Books:
[T2] Stephanopoulos G. “Chemical Process Control: An Introduction to Theory and Practice”, PHI

Reference Books:
### DATABASE MANAGEMENT SYSTEMS

**Paper Code:** ETCS-425  
**Paper:** Database Management Systems  
**L** 3  
**T/P** 0  
**C** 3

**INSTRUCTIONS TO PAPER SETTERS:**  
**MAXIMUM MARKS:** 75

1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.
2. Apart from Question No. 1, rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be 12.5 marks.

**Objective:** The concepts related to database, database techniques, SQL and database operations are introduced in this subject. This creates strong foundation for application data design.

**UNIT-I : Introductory Concepts of DBMS:**  
Introduction and application of DBMS, Data Independence, Database System Architecture – levels, Mapping; Database users and DBA, Entity – Relationship model, constraints, keys, Design issues, E-R Diagram, Extended E-R features- Generalization, Specialization, Aggregation, Translating E-R model into Relational model.  

**UNIT-II : Relational Model:**  

**UNIT-III:** Functional Dependencies, Non-loss Decomposition, First, Second, Third Normal Forms, Dependency Preservation, Boyce/Codd Normal Form, Multi-valued Dependencies and Fourth Normal Form, Join Dependencies and Fifth Normal Form.

**UNIT-IV:** Transaction Management:  
ACID properties, serializability of Transaction, Testing for Serializability and concurrency control, Lock based concurrency control (2PL, Deadlocks), Time stamping methods, Database recovery management.

**Implementation Techniques:** Overview of Physical Storage Media, File Organization, Indexing and Hashing, B+ tree Index Files, Query Processing Overview, Catalog Information for Cost Estimation, Selection Operation, Sorting, Join Operation, Materialized views, Database Tuning.

**Text Books:**  

**References Books:**  
RENEWABLE ENERGY RESOURCES

Paper Code: ETEE-419
Paper: Renewable Energy Resources

### INSTRUCTIONS TO PAPER SETTERS:

**MAXIMUM MARKS: 75**

1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.
2. Apart from Q. No. 1 rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks.

---

**Objective:** The objective of the paper is to introduce the knowledge of upcoming and future promising area of renewable energy resources to the students, which is developing rapidly.

### UNIT-I
Solar Energy: radiation – extra terrestrial, spectral distribution, solar constant, solar radiation on earth, measurements; solar thermal system – solar thermal power and its conversion, solar collectors, flat plate, solar concentrating collectors, - types and applications, photovoltaic (PV) technology - photovoltaic effect, efficiency of solar cells, semi-conductor materials, solar PV system, standards and applications, tracking.

[T1][No. of hrs. 10]

### UNIT-II
Wind and Small Hydropower Energy: wind data, properties, speed and power relation, power extracted, wind distribution and speed prediction, wind map of India; wind turbines and electric generators. fundamentals – types of machines and their characteristics, horizontal and vertical wind mills, elementary design principle, wind energy farms, off-shore plants; small, mini and micro hydro power plants and their resource assessment, plant layout with major components shown.

[T2][No. of hrs. 10]

### UNIT-III
Other Non-conventional Energy Sources: biomass – photosynthesis and origin of biomass energy, resources, cultivated resources, waste to biomass; terms and definitions – incineration, wood and wood waste, harvesting super tree, energy forest, phyrolysis, thermo-chemical biomass conversion to energy, gasification, anaerobic digester, fermentation, gaseous fuel; geothermal – resources, hot spring, steam system, principle of working, site selection, associated problems in development; ocean and tidal energy – principle of ocean thermal energy conversion, wave energy conversion machines, problems and limitations, fundamentals of tidal power, conversion systems and limitations; hydrogen energy – properties of hydrogen, sources, production and storage, transportation, problems for use as fuel; fuel cells – introduction with types, principle of operation and advantages.

[T1,R2][No. of hrs. 12]

### UNIT-IV
Grid Connectivity: wind power interconnection requirement - low-voltage ride through (LVRT), ramp-rate limitations, supply of ancillary services for frequency and voltage control, load following, reserve requirement, impact of connection on steady-state and dynamic performance of power system; interfacing dispersed generation of solar energy with the grid, protective relaying, islanding, voltage flicker and other power quality issues; role of non-conventional energy system in smart grid.

[T2,R3][No. of hrs. 10]

**Text Books:**

**References Books:**
SELECTED TOPICS IN ICE

Paper Code: ETIC-417
Paper: Selected Topics in ICE

<table>
<thead>
<tr>
<th>INSTRUCTIONS TO PAPER SETTERS:</th>
<th>MAXIMUM MARKS: 75</th>
</tr>
</thead>
<tbody>
<tr>
<td>1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.</td>
<td></td>
</tr>
<tr>
<td>2. Apart from Q. No. 1 rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks.</td>
<td></td>
</tr>
</tbody>
</table>

Objectives: To study and understand a few topics of current interest and utility to control and instrumentation Engineers.

UNIT I

Power Plant Instrumentation: Types of power plants, thermal/hydro/nuclear/non-conventional. Overall scheme from generation to power station terminals, number and types of various parameters monitored. Signal Pick up, transmission, communication to control room.

[T1] [No. of Hours:11]

UNIT II

Control Room: Lay out and details, parameter monitoring in display and recording, log sheets, Inhouse parameter monitoring and recording. Generation parameter monitoring and recording, Control of critical parameters, P and I diagrams of important loops.

[T1] [No. of Hours:12]

UNIT III


[T2] [No. of Hours:10]

UNIT IV

Non Destructive Testing:
Basic philosophy and importance of NDT techniques, critical parameters/status/malfunctioning/identification. Applications of various techniques and case studies in respect of various industrial products.

[T1][T2] [No. of Hours:11]

Text Books:
[T2] L. G. Lasithan, "Mechanical Vibrations and Industrial noise control", PHI

Reference Books:
[R3] Journals and Publications on NDT
ENGINEERING MATERIALS

Paper Code: ETIC-419

Paper: Engineering Materials

<table>
<thead>
<tr>
<th>L</th>
<th>T/P</th>
<th>C</th>
</tr>
</thead>
<tbody>
<tr>
<td>3</td>
<td>0</td>
<td>3</td>
</tr>
</tbody>
</table>

**INSTRUCTIONS TO PAPER SETTERS:**

MAXIMUM MARKS: 75

1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.
2. Apart from question no. 1, rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks.

Objective: The objective of the paper is to facilitate the students with the field concerned with the design, manufacture and use of all classes of materials.

UNIT- I


Crystal Geometry & Structure Determination: Geometry of crystal, structure determination by X-Ray diffraction.


UNIT- II

Structure of solid: Crystalline states & non crystalline states; Inorganic & alloy, structure of silica & silicates, Polymers- classification, structure & crystallinity of long chain polymers.

Crystal Imperfection: point imperfection, geometry of dislocation, surface imperfection.

Phase Diagram: phase rule, single component system, binary phase diagram, the lever rule.


UNIT- III

Oxidation & Corrosion: Mechanism of oxidation, oxidation resistant materials, principles of corrosion, protection against corrosion.

Electrical properties of materials: Electrical conductivity, electron mobility, resistance, energy gaps in solids.

Mechanical properties of metals: Stress, strain, elasticity, tensile strength

Conductors & Resistors: resistivity range, free electron theory, conduction by free electrons, conductor and resistor materials, superconducting materials


UNIT- IV

Polymers: Structure, characteristics and applications.

Composites: Introduction, Particle reinforced composites, fiber reinforced composites & structural composites.

Magnetic Materials: terminology & classification, magnetic moments due to electron spin, ferromagnetism and related phenomena, the domain structure, soft and hard magnetic materials.

Dielectric Materials: polarization, temperature & frequency effects, electric breakdown, ferromagnetic materials.

Text Books:


Reference Books:

COMPUTER ARCHITECTURE

Paper Code: ETIC-421
Paper: Computer Architecture

<table>
<thead>
<tr>
<th>L</th>
<th>T/P</th>
<th>C</th>
</tr>
</thead>
<tbody>
<tr>
<td>3</td>
<td>0</td>
<td>3</td>
</tr>
</tbody>
</table>

INSTRUCTIONS TO PAPER SETTERS:

1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.
2. Apart from Question No. 1, the rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, the student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks.

UNIT- I
Computer Arithmetic and Register transfer language:
Unsigned notation, signed notation, binary coded decimal, floating point numbers, IEEE 754 floating point standard, Micro-operation, Bus and Memory Transfers, Bus Architecture, Bus Arbitration, Arithmetic Logic, Shift Micro operation, Arithmetic Logic Shift Unit.

UNIT- II
Instruction set architecture & computer organization:
Levels of programming languages, assembly language instructions, 8085 instruction set architecture, Instruction Codes, Computer Registers, Computer Instructions, Timing & Control, Instruction Cycle, Memory Reference Instructions, Input-Output and Interrupts

UNIT- III
Control Design:
Instruction sequencing & interpretation, Hardwired & Micro Programmed (Control Unit), Microprogrammed computers, Microcoded CPU: Pentium processor. Specifying a CPU, Design & implementation of simple CPU, General Register Organization, Stack Organization, Instruction Formats, Addressing Modes, Internal architecture of 8085 microprocessor.

UNIT- IV
Memory & Input/Output organization:
Memory Technology, Main Memory (RAM and ROM Chips), Virtual memory, High-speed memories, Asynchronous Data Transfers, Programmed I/O, interrupts, Direct memory Access, Serial communication, UARTs, RS-232-C & RS-422 standard.

Text Books:

Reference Books:

Objective: The objective of the paper is to be introducing the student about the computer arithmetic and register transfer language, control design and internal architecture of 8085 microprocessor.
SOFTWARE ENGINEERING

Paper Code: ETIC-423
Paper: Software Engineering

L T/P C

3 0 3

INSTRUCTIONS TO PAPER SETTERS:

MAXIMUM MARKS: 75

1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.

2. Apart from question no. 1, rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks.

Objective: To make the student well versed with the current software requirements of the industry.

UNIT I

UNIT II
Software measurement - Software process improvement by Size Metrics like LOC, Token Count, Function Count, Design Metrics, Data Structure Metrics, Information Flow Metrics.

UNIT III
Coding And Implementation - Top-down and Bottom-up approaches, structured programming, Object Oriented Programming.
Software Testing: Functional testing: Boundary value analysis, Equivalence class testing, Decision table testing, Cause effect graphing, Structural testing: Path testing, Data flow and mutation testing, unit testing, integration and system testing, Debugging, Testing Tools & Standards.

UNIT IV

Text Books:

References Books:
OPERATING SYSTEMS

Paper Code: ETIC-427        L  T/P  C
Paper: Operating Systems 3  0  3

INSTRUCTIONS TO PAPER SETTERS: MAXIMUM MARKS: 75
1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.
2. Apart from question no. 1, rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks.

Objective: The goal of this course is to provide an introduction to the internal operation of modern operating systems. The course will cover processes and threads, mutual exclusion, CPU scheduling, deadlock, memory management, and file systems.

UNIT I


Memory Organization & Management: Memory Organization, Memory Hierarchy, Memory Management Strategies, Contiguous versus non-Contiguous memory allocation, Partition Management Techniques, Logical versus Physical Address space, swapping, Paging, Segmentation, Segmentation with Paging

Virtual Memory: Demand Paging, Page Replacement, Page-replacement Algorithms, Performance of Demand Paging, Thrashing, Demand Segmentation, Overlay Concepts

UNIT II


Processor Scheduling: Scheduling levels, pre-emptive vs non-pre-emptive scheduling, priorities, scheduling objective, scheduling criteria, scheduling algorithms, demand scheduling, real-time scheduling.

Process Synchronization: Mutual exclusion, software solution to Mutual exclusion problem, hardware solution to Mutual exclusion problem, semaphores, Critical section problems. Case study on Dining philosopher problem, Barber shop problem etc.

UNIT III

Deadlocks: examples of deadlock, resource concepts, necessary conditions for deadlock, deadlock solution, deadlock prevention, deadlock avoidance with Bankers algorithms, deadlock detection, deadlock recovery

Device Management: Disk Scheduling Strategies, Rotational Optimization, System Consideration, Caching and Buffering

UNIT IV


Text Books:

References Books:
SOCIOLOGY AND ELEMENTS OF INDIAN HISTORY FOR ENGINEERS

Paper Code: ETHS-419
Paper: Sociology and Elements of Indian History for Engineers

<table>
<thead>
<tr>
<th>INSTRUCTIONS TO PAPER SETTERS:</th>
<th>MAXIMUM MARKS: 75</th>
</tr>
</thead>
<tbody>
<tr>
<td>1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.</td>
<td></td>
</tr>
<tr>
<td>2. Apart from Question No. 1, rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks.</td>
<td></td>
</tr>
</tbody>
</table>

Objective: The objective of this course is to familiarize the prospective engineers with elements of Indian history and sociological concepts and theories by which they could understand contemporary issues and problems in Indian society. The course would enable them to analyze critically the social processes of globalization, modernization and social change. All of this is a part of the quest to help the students imbibe such skills that will enhance them to be better citizens and human beings at their work place or in the family or in other social institutions.

UNIT I
Module 1A: Introduction to Elements of Indian History: What is history? ; History Sources-Archaeology, Numismatics, Epigraphy & Archival research; Methods used in History; History & historiography; [3 Lectures]

Module 1B: Introduction to sociological concepts-structure, system, organization, social institution, Culture social stratification (caste, class, gender, power). State & civil society; [7 Lectures] [T1][No. of Hrs. 10]

UNIT II
Module 2A: Indian history & periodization; evolution of urbanization process: first, second & third phase of urbanization; Evolution of polity; early states of empires; Understanding social structures-feudalism debate; [3 Lectures]

Module 2B: Understanding social structure and social processes: Perspectives of Marx, Weber & Durkheim; [7 Lectures] [T1][No. of Hrs. 10]

UNIT III
Module 3A: From Feudalism to colonialism-the coming of British; Modernity & struggle for independence; [3 Lectures]

Module 3B: Understanding social structure and social processes: Perspectives of Marx, Weber & Durkheim; [9 Lectures] [T1][No. of Hrs. 12]

UNIT IV
Module 4A: Issues & concerns in post-colonial India (upto 1991); Issues & concerns in post-colonial India 2nd phase (LPG decade post 1991); [3 Lectures]

Module 4B: Social change in contemporary India: Modernization and globalization, Secularism and communalism, Nature of development, Processes of social exclusion and inclusion, Changing nature of work and organization. [10 Lectures] [T1][No. of Hrs. 13]

Text Books:

Reference Books:
[R1] Guha, Ramachandra (2007), India After Gandhi, Pan Macmillan
**DIGITAL CONTROL SYSTEMS LAB**

**Paper Code:** ETIC–451  
**Paper:** Digital Control Systems Lab  
**L T/P C:** 0 2 1

**List of Experiments:**

**Write MATLAB Programs for the following:**

1. Obtain the inverse Z–transform of the following:
   \[ X(z) = \frac{z^2+2z}{Z^2-2z+1} \text{ and } f(z)=z(z+1) \]

2. For the LTI systems described by the following difference equations generate its impulse response and unit step response:
   1. \[ Y(n) = x(n)+2x(n-1) \]
   2. \[ Y(n)=0.9y(n-1)+x(n) \] also find the analytical expression
   3. \[ Y(n)=0.3695y(n-1)+0.1958y(n-2)= 0.2066x(n)+0.4131x(n-1)+0.2066x(n-2) \]

3. For the discrete-time transfer function
   \[ H(z) = \frac{0.25z^3-0.6273z^2+0.5153z-0.1367}{Z^3-2.811z^2+2.652z-0.8395} \]
   - Transfer function to polo-zero conversion
   - Draw the polo-zero plot
   - Polo-zero to transfer function
   - Find the partial fraction expression of the transfer function
   - \( r, p, k \) to transfer function
   - Root locus and stability analysis

4. Find the ZOH equivalent transfer function of \( \frac{10}{5s+1} \) Obtained with the sampling period \( T_s=0.5 \text{sec.} \)

5. Obtain the six different realizations of the pulse transfer function
   \[ \frac{y(z)}{u(z)} = \frac{z+3}{z^3+9z^2+24z+20} \]

6. For the sampled data system shown in fig. below, find the response to step input
7. Find the gain for stability.

8. Determine whether the system is observable and state controllable.

\[
x = \begin{bmatrix} -2 & -1 & -3 \\ 0 & -2 & 1 \\ -7 & -8 & -9 \end{bmatrix} x + \begin{bmatrix} 2 \\ 1 \\ 1 \end{bmatrix} u; y = \begin{bmatrix} 4 & 6 & 8 \end{bmatrix} x
\]

9. Obtain bilinear transformation for
   (i) \( z^3 - 1.3z^2 + 0.08z + 0.24 = 0 \) and analyze the stability.
   (ii) Obtain the root locus and analyze the stability.

10. Find the pulse transfer function of the given continuous-time system given below

    \[
    A = \begin{bmatrix} 0 & 0.1 \\ 1 & -0.1 \end{bmatrix}; B = \begin{bmatrix} 0.1 \\ 0 \end{bmatrix}; C = \begin{bmatrix} 0 \\ 1 \end{bmatrix}; D = 0 \\
    \text{For sampling time } T_s = 0.2 \text{ s.}
    \]

**NOTE:** At least 8 Experiments out of the list must be done in the semester.
BIOMEDICAL INSTRUMENTATION LAB

Paper Code: ETIC-453
Paper: Biomedical Instrumentation Lab

List of Experiments:

1. To study various transducers for biomedical applications
2. To study various functions of Bedside & Central Patient Monitoring Unit.
3. To measure blood pressure using Patient Monitoring Unit.
4. To study working principle & measure blood pressure using Sphygmomanometer.
5. To measure percentage amount of oxygenated arterial blood using Patient Monitoring Unit.
6. To measure ECG using Patient Monitoring Unit.
7. To measure body temperature using Patient Monitoring Unit.
8. To study working principle & measure body temperature using Digital Thermometer.

NOTE:- At least 8 Experiments out of the list must be done in the semester.
ELECTRICAL DRIVES LAB

Paper Code: ETIC-461(ELECTIVE)  L  T/P  C
Paper: Electrical Drives Lab  0  2  1

List of Experiments:

1. Load equalization by flywheel for intermittent duty loads.
2. Comparison of various braking methods and their range of braking for induction motor.
3. Open loop AC voltage Control of single phase capacitor run induction motor.
4. Verification of linear relationship between duty cycle vs speed in open loop step down chopper controlled DC motor drive.
5. Single phase thyristorised full converter fed closed loop speed control of DC motor drive.
6. Closed loop speed control of 4 quadrant DC motor drive.
7. Closed Loop constant v/f speed control of Induction motor drive.
8. Closed Loop speed control through static rotor resistance controlled slip ring Induction motor.
10. Closed Loop speed control of SRM drive.

NOTE:- At least 8 Experiments out of the list must be done in the semester.
DATABASE MANAGEMENT SYSTEMS LAB

Paper Code: ETIC-461(ELECTIVE)  L  T/P  C
Paper: Database Management Systems Lab  0  2  1

LAB BASED ON DBMS
Lab includes implementation of DDL, DCL, DML i.e SQL in Oracle.

List of Experiments:
1. Design a Database and create required tables, For e.g. Bank, College Database
2. Apply the constraints like Primary Key, Foreign key, NOT NULL to the tables.
3. Write a SQL statement for implementing ALTER, UPDATE and DELETE
4. Write the queries to implement the joins
5. Write the queries for implementing the following functions: MAX (), MIN (), AVG (), COUNT ()
6. Write the queries to implement the concept of Integrity constrains
7. Write the queries to create the views
8. Perform the queries for triggers
9. Perform the following operation for demonstrating the insertion, updatation and deletion using the referential integrity constraints

TEXT BOOK:

NOTE:- At least 8 Experiments out of the list must be done in the semester.
SOFTWARE ENGINEERING

Paper Code: ETIC-461(ELECTIVE)            L  T/P  C
Paper: Software Engineering             0  2   1

Required: Rational Rose Enterprise Edition

List of Experiments:

1. Write down the problem statement for a suggested system of relevance.
2. Do requirement analysis and develop Software Requirement Specification Sheet (SRS) for suggested system.
3. To perform the function-oriented diagram: Data Flow Diagram (DFD) and Structured chart.
4. To perform the user’s view analysis for the suggested system: Use case diagram.
5. To draw the structural view diagram for the system: Class diagram, object diagram.
6. To draw the behavioral view diagram: State-chart diagram, Activity diagram.
7. To perform the behavioral view diagram for the suggested system: Sequence diagram, Collaboration diagram.
8. To perform the implementation view diagram: Component diagram for the system.
9. To perform the environmental view diagram: Deployment diagram for the system.
10. To perform various testing using the testing tool unit testing, integration testing for a sample code of the suggested system.
11. Perform Estimation of effort using FP Estimation for chosen system.
12. To prepare time line chart/Gantt Chart/PERT Chart for selected software project.

Text Books:

NOTE:- At least 8 Experiments out of the list must be done in the semester.
HUMAN VALUES & PROFESSIONAL ETHICS – II

Paper Code: ETHS-402
Paper : Human Values & Professional Ethics-II

L T/P C
1 0 1

INSTRUCTIONS TO PAPER SETTERS:

MAXIMUM MARKS: 75

1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.
2. Apart from Question No. 1, rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks.
3. Two internal sessional test of 10 marks each and one project report* carrying 5 marks.

Objectives
1. The main object of this paper is to inculcate the skills of ethical decision making and then to apply these skills to the real and current challenges of the engineering profession.
2. To enable student teachers to understand the need and importance of value-education and education for Human Rights.
3. To acquaint students to the National and International values for Global development

UNIT I - Appraisal of Human Values and Professional Ethics:


Sensitization of Impact of Modern Education and Media on Values:

a) Impact of Science and Technology
b) Effects of Printed Media and Television on Values
c) Effects of computer aided media on Values (Internet, e-mail, Chat etc.)
d) Role of teacher in the preservation of tradition and culture.
e) Role of family, tradition & community prayers in value development.

Review of Professional Ethics: Accountability, Collegiality, Royalty, Responsibility and Ethics Living.

Engineer as a role model for civil society, Living in harmony with ‘NATURE’, Four orders of living, their intercorrectness, Holistic technology (eco-friendly and sustainable technology).

UNIT II – Engineers responsibility for safety:


Some Case Studies: Case Studies, BHOFAL Gas Tragedy, Nuclear Power Plant Disasters, Space Shuttle Challenger, Three Mile Island Accident, etc.

UNIT III – Global Issues:

Case Studies: Kelleg’s, Satyam, Infosys Foundation, TATA Group of Companies
Business Ethics: Corporate Governance, Finance and Accounting, IPR.
Corporate Social Responsibility (CSR): Definition, Concept, ISO, CSR.
Environmental Ethics: Sustainable Development, Eco-System, Ozone depletion, Pollution.
Computer Ethics: Cyber Crimes, Data Stealing, Hacking, Embezzlement.

UNIT IV - Engineers Responsibilities and Rights and Ethical Codes:

Collegiality and loyalty, Conflict of interests, confidentiality, occupational crimes, professional rights, responsibilities. To boost industrial production with excellent quality and efficiency, To enhance national economy, To boost team spirit, Work Culture and feeling of job satisfaction, National integration, Examples of some illustrious professionals.

Need for Ethical Codes, Study of some sample codes such as institution of Electrical and Electronics Engineers, Computer Society of India etc., Ethical Audit.

Development and implementation of Codes: Oath to be taken by Engineering graduates and its importance**

Scheme and Syllabi for B. Tech-ICE, 1st year (Common to all branches) w.e.f batch 2014-15 and (2nd, 3rd & 4th years) w.e.f batch 2013-14 approved in the 22nd BOS of USET on 30th June, 2014 and approved in the 37th AC Sub Committee Meeting held on 10th July, 2014.
Text Books:
[T1] Professional Ethics, R. Subramanian, Oxford University Press.

References Books:
[R9] PSR Murthy, “Indian Culture Values and Professional Ethics”, BS Publications
[R10] Caroline Whitback< Ethics in Engineering Practice and Research, Cambriges University Press
[R14] M. Shery; Bhartiya Sanskriti, Agra (Dayalbagh)

# Non-University Examination Scheme (NUES)

*Any topic related to the experience of the B.Tech student in the assimilation and implementation of human values and professional ethics during the past three years of his/her studies in the institute OR A rigorous ethical analysis of a recent case of violation of professional ethics particularly related to engineering profession.

**All students are required to take OATH in writing prior to submission of major project and the record of the same is to be maintained at the college level and/or, this oath may be administered by the head of the institutions during the graduation ceremonies. The draft for the same is available alongwith the scheme and syllabus.
OATH TO BE TAKEN BY ENGINEERING GRADUATES

In a manner similar to the Hippocratic Oath taken by the medical graduates, Oath to be taken by the engineering graduates is as given below.

1. I solemnly pledge myself to consecrate my life to the service of humanity.
2. I will give my teacher the respect and gratitude, which is their due.
3. I will be loyal to the profession of engineering and be just and generous to its members.
4. Whatever project I undertake, it will be for the good of mankind.
5. I will exercise my profession solely for the benefit of humanity and perform no act for criminal purpose and not contrary to the laws of humanity.
6. I will keep away from wrong, corruption and avoid tempting others to vicious practices.
7. I will endeavor to avoid waste and consumption of non-renewable resources.
8. I will speak out against evil and unjust practices whenever and wherever I encounter them.
9. I will not permit considerations of religion, nationality, race, party politics or social standing to intervene between my duty and my work, even under threat.
10. I will practice my profession with conscience, dignity and uprightness.
11. I will respect the secrets, which are confided to me.

I make these promises solemnly, freely and upon my honor.

(Name of the Student)
Correspondence Address: ________________________________
_________________________________
_________________________________
Email: _________________________________
INTELLIGENT SYSTEMS AND CONTROL

Paper Code: ETIC-404

Paper: Intelligent Systems and Control

L T/P C
3 1 4

INSTRUCTIONS TO PAPER SETTERS:

MAXIMUM MARKS: 75

1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.

2. Apart from question no. 1, rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks.

Objective: To give students concept of intelligent control with fuzzy logic and methodologies developed in the field of soft-computing which can lead to accommodation of more complex processes, improved performance and considerable time savings and cost reductions.

UNIT - I
Basic concepts of Fuzzy logic: Fuzzy sets, operations of Fuzzy sets, properties of Fuzzy sets, Fuzzy relations, composition of Fuzzy relation, cylindrical extension, projection, Different implications of Fuzzy If-Then rules, Fundamental issues in Control Engineering, Mamdani architecture for Fuzzy Control, The Sugeno Takagi architecture, FKBC Design Parameter: The FKBC architecture, choice of variable and contents of rules, knowledge representation in KBC and derivation of rules, choice of membership functions, choice of scaling factors, choice of fuzzification procedures, choice of defuzzification procedures, comparison and evaluation of defuzzification methods.

UNIT - II
Nonlinear and Adaptive Fuzzy Control: The FKBC as a Non-Linear Transfer Element, Types of FKBC such as PID-like FKBC, sliding Mode FKBC, Performance Evaluation, Approaches to Design such as membership function tuning using gradient descent, membership function tuning using performance criteria, the self-organizing controller, model based controller Stability of Fuzzy Control Systems: State space Approach, Stability & Robustness indices, Input-output stability, The Circle criterion, The Conicity criterion.

UNIT - III

UNIT – IV

Text Books:
[T1] John Yen, Reza Langari, “Fuzzy Logic Intelligence, Control and Information” Pearson

Reference Books:
VIRTUAL INSTRUMENTATION

Paper Code: ETIC–406
Paper: Virtual Instrumentation

<table>
<thead>
<tr>
<th>INSTRUCTIONS TO PAPER SETTERS:</th>
<th>MAXIMUM MARKS: 75</th>
</tr>
</thead>
<tbody>
<tr>
<td>1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.</td>
<td></td>
</tr>
<tr>
<td>2. Apart from question no. 1, rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks.</td>
<td></td>
</tr>
</tbody>
</table>

Objective: Virtual Instrumentation gives students a concept for the design and integration of customized applications for Instrumentation, automation and control systems in real time.

UNIT - I
Introduction to LabVIEW: Definition and architecture of virtual instrumentation system, salient features and application area of virtual instrumentation, Detail of Software environment, loops, structures and tunnels, arrays, clusters, plotting data

UNIT - II
Data Flow Programming Techniques: Graphical programming in data flow, comparison with conventional programming, popular data flow and VI software packages. Building a VI front panel and block diagram, sub VI, for and while loops, case and sequence structure, formula nodes, local and global variables, string and file I/O, array and clusters, charts and graphs, attributes nodes

UNIT - III
Instrument Control: GPIB communication, hardware and software architecture and specifications, instrument I/O assistant, VISA, Instrument Drivers, Serial Port communications.

Data Acquisition: Transducers, signal conditioning, DAQ hardware configuration, DAQ hardware, Analogy I/O, Counters, Digital I/O, DAQ assistant, selecting and configuring a data acquisition device.

UNIT – IV
IMAQ Vision: Vision basics, image processing and analysis, particle analysis, machine vision, machine vision hardware and software, building a complete machine vision system.

Text Books:

Reference Books:
SYSTEM MODELLING & SIMULATION

Paper Code: ETIC-408
Paper: System Modelling & Simulation

L T/P C
3 0 3

INSTRUCTIONS TO PAPER SETTERS:
MAXIMUM MARKS: 75
1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.
2. Apart from question no. 1, rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks.

Objective: The objective of the paper is to facilitate the students with the knowledge how something will behave without actually testing it in real life. To ensure that the results of simulation are applicable to the real world, students must understand the assumptions, conceptualizations, and implementation constraints of this emerging field.

UNIT - I
System Concept and System Models: Concept of system, stochastic activities, Continuous and discrete systems, system modelling, principles of modelling, types of models- static physical, dynamic physical, static mathematical, dynamic mathematical.
Introduction to Simulation: Need for simulation, steps of simulation models, types of simulation, discrete event simulation- Monte- Carlo simulation, estimation of demand of product & determination of the value of \( \pi \). Cobweb models, Progress of a simulation study.

UNIT - II
Continuous system simulation: continuous system models, differential equations, analog & hybrid computers, analog methods, digital analog simulator, hybrid simulation, feedback systems & real time simulations
System Dynamics: Exponential growth & decay models, logistic curves, generalization of growth models, system dynamics diagrams, multi segment models, representation of time delays, feedback in socio-economic systems.

UNIT - III
Methods of Random Number Generation & their tests: Introduction, Mid Square Method, Multiplicative Congruential Method, Combined Multiple Recursive Generator, Testing Randomness of Random Numbers: Chi-Square Method, Kolmogorov-Smirnov Test, Bartels Ratio Test, Run test, run up & down test.

UNIT - IV
Queueing Theory: Introduction, Terminologies, Empirical Queueing Models: (M/M/1) queues, finite capacity queues, multiple servers, (M/M/c) queues. Simulation with high level languages.
Discrete system simulation: discrete events, representation of time, generation of arrival pattern, simulation of telephone system, simulation programming tasks.

Text Books:

Reference Books:
**EMBEDDED SYSTEMS**

**Paper Code: ETIC-410**  
**Paper: Embedded Systems**  
**L T/P C**  

<table>
<thead>
<tr>
<th><strong>INSTRUCTIONS TO PAPER SETTERS:</strong></th>
<th><strong>MAXIMUM MARKS: 75</strong></th>
</tr>
</thead>
<tbody>
<tr>
<td>1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.</td>
<td></td>
</tr>
<tr>
<td>2. Apart from Question No. 1, rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks</td>
<td></td>
</tr>
</tbody>
</table>

**Objective:** The objective of the paper is to enable a student to design an embedded system for specific tasks.

**UNIT- I**  

**PIC Microcontrollers:** Architecture, Registers, memory interfacing, interrupts, instructions, programming and peripherals.  

**UNIT- II**  
**ARM Processors:** Comparison of ARM architecture with PIC micro controller, ARM 7 Data Path, Registers, Memory Organization, Instruction set, Programming, Exception programming, Interrupt Handling, Thumb mode Architecture.  
Bus structure: Time multiplexing, serial, parallel communication bus structure, Bus arbitration, DMA, PCI, AMBA, I2C and SPI Buses.

**UNIT- III**  
Embedded Software, Concept of Real Time Systems, Software Quality Measurement, Compilers for Embedded System

**UNIT-IV**  

**Text book:**  
[T1] Design with PIC Microcontrollers, John B. Peatman, Pearson Education Asia, 2002  

**References Books:**  
[R1] The Design of Small-Scale embedded systems, Tim Wilmhurst, Palgrave2003  
**DIGITAL IMAGE PROCESSING**

**Paper Code:** ETIT-418  
**Paper:** Digital Image Processing  
**L**  **T/P**  **C**  
3  0  3

<table>
<thead>
<tr>
<th>INSTRUCTIONS TO PAPER SETTERS:</th>
<th>MAXIMUM MARKS: 75</th>
</tr>
</thead>
<tbody>
<tr>
<td>1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.</td>
<td></td>
</tr>
<tr>
<td>2. Apart from question no. 1, rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks.</td>
<td></td>
</tr>
</tbody>
</table>

**Objectives:** The aim of this course is to provide digital image processing fundamentals, hardware and software, digitization, encoding, segmentation, feature extraction etc. It will enhance the ability of students to apply tools in image restoration, enhancement and compression and to apply the techniques in both the spatial and frequency domains. It will enhance the ability of students to identify the quality characteristics of medical images, differences between computer vision and image processing and help in studying the remote sensing images of the environmental studies.

**UNIT-I:**

**Image Enhancement in the Spatial Domain:** Some basic Gray Level Transformations, Histogram Processing, Enhancement Using Arithmetic and Logic operations, Basics of Spatial Filters, Smoothening and Sharpening Spatial Filters, Combining Spatial Enhancement Methods.

**UNIT-II:**
**Filtering in the Frequency Domain:** Introduction to Fourier Transform and the frequency Domain, Smoothing and Sharpening Frequency Domain Filters.


**UNIT-III:**
**Image Compression:** fundamentals of compression, coding redundancy, Lossy and lossless compression, Spatial and temporal redundancy, Image compression models. Some basic compression methods

**Image Segmentation:** Detection of Discontinuities, Edge linking and boundary detection, Region Oriented Segmentation, Motion based segmentation.

**UNIT-IV:**
**Representation and Description:** Representation, Boundary Descriptors, Regional Descriptors, Use of Principal Components for Description, Introduction to Morphology, Some basic Morphological Algorithms.

**Object Recognition:** Patterns and Pattern Classes, Decision-Theoretic Methods, Structural Methods.

**Text Books:**

**Reference Books:**
### VLSI DESIGN

**Paper Code:** ETIC-414  
**Paper:** VLSI Design  

<table>
<thead>
<tr>
<th>L</th>
<th>T/P</th>
<th>C</th>
</tr>
</thead>
<tbody>
<tr>
<td>3</td>
<td>0</td>
<td>3</td>
</tr>
</tbody>
</table>

#### INSTRUCTIONS TO PAPER SETTERS:

1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.
2. Apart from Q. No. 1 rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks.

---

**Objective:** The prerequisite are analog devices, STLD, Digital system design and micro-electronics. The students are introducing to MOS technology, design rules and some applications.

#### UNIT I
Evolution of VLSI, MOS transistor theory, MOS structure, enhancement & depletion transistor, threshold voltage, MOS device design equations, MOSFET scaling and small geometry effects, MOSFET capacitances. 
NMOS inverter, CMOS inverter, DC characteristics, static load MOS inverter, pull up/pull down ratio, static & dynamic power dissipation, CMOS & NMOS process technology – explanation of different stages in fabrication, body effect, latch up in CMOS.

[T1,T2][No. of Hours: 11]

#### UNIT II
Stick diagram and design rules, lambda based design rules, switching characteristics & inter connection effects: rise time, fall time delays, noise margin.
CMOS logic gate design: NAND, NOR, XOR and XNOR gates, Transistor sizing, combinational MOS logic circuits: pass transistor and transmission gate designs, Pseudo NMOS logic.

[T1,T2][No. of Hours: 11]

#### UNIT III
Sequential MOS logic circuits: SR latch, clocked latch and flip flop circuits, CMOS D latch and edge triggered flip flop, dynamic logic circuits; basic principle, non ideal effects, domino CMOS logic, high performance dynamic CMOS circuits, clocking issues, clock distribution.

[T1,T2][No. of Hours: 11]

#### UNIT IV
VLSI designing methodology, design flow, design Hierarchy, concept of regularity, modularity & locality, VLSI design style, Design quality, computer aided design technology, adder design and multiplier design examples. Low power design concepts using CMOS Technology.

[T1,T2][No. of Hours: 11]

#### Text Books:


#### Reference Book:


---

Scheme and Syllabi for B. Tech-ICE, 1st year (Common to all branches) w.e.f batch 2014-15 and (2nd, 3rd & 4th years) w.e.f batch 2013-14 approved in the 22nd BOS of USET on 30th June, 2014 and approved in the 37th AC Sub Committee Meeting held on 10th July, 2014.
CONSUMER ELECTRONICS

Paper Code: ETEC-408
Paper: Consumer Electronics

INSTRUCTIONS TO PAPER SETTERS:

MAXIMUM MARKS: 75

1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.

2. Apart from question no. 1, rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks.

Objective: The objective of teaching this subject is to give students in depth knowledge of various electronic audio and video devices and systems. Further this subject will introduce the students with working principles, block diagram, main features of consumer electronics gadgets/goods/devices like audio-systems, CD systems, TV, VCR and other items like fax machine washing machine, microwave ovens, digital camera & iPODS etc., which in-turn will develop in them capabilities of assembling, fault diagnosis and rectification in a systematic way.

UNIT I
Audio System: Microphones, Construction, Working principles and applications of microphone: Carbon, Moving coil, velocity, crystal, condenser type, Cordless microphone, Dynamic & wireless microphone. Loud Speakers: Direct radiating, horn loaded, woofer, tweeter and squeaker, baffles and enclosures. Sound recording on magnetic tape its principles, block diagram and tape transport mechanism, Wow, Flutter & Rumble distortion. Relationship between gap width, tape speed and frequency. Optical recording and reproduction system, Blue ray technology, VCD & DVD system, HI- Fi system, condition for good acoustic features, stereo amplifiers.

UNIT II
Television: Monochrome TV Communication: Elements of TV communication system; Scanning – its need for picture transmission; Need synchronizing and blanking pulses; Progressive scanning, interlaced scanning, ell effect, resolution and band width requirement, Composite Video signal (CVS) at the end of even and odd fields, advantage & disadvantage of negative modulation, need of pre & post Equalizing pulses; Monochrome picture tube– construction and working, comparison of magnetic and electric of Construction and working of camera tube; vidicon and plumbicon, night vision camera. Block diagram of a TV receiver: function of each block and wave form at the input and output of each block; Frequency range of various VHF bands and channels used in India, Major specification of the CCIR B standard. Typical circuits of scanning and EHT stages of TV receiver, keyed AGC,SAW filter; trap circuit, Identification of faulty stage by analyzing the symptoms and basic idea of a few important faults and there remedies.

UNIT III
Color TV: Primary colors, trisimulus values, trichromite coefficients, concepts of additive and subtracting mixing of colour, concepts of luminance, Hue and saturation, Compatibility of colour TV system with monochrome system: Block diagram of colour TV camera; Construction and working principles of Trinitron, delta gun and PIL types of colour picture tubes, Concepts of degaussing, purity, beam shifting; burst signal and its need, chrominance signal; analysis of G-Y signal is not transmitted, Block diagram of PAL TV receiver.

UNIT IV
Comparison of digital TV LCD, LED, HDTV, Plasma TV & Three dimension TV. Cable Television: Block diagram and principle of working of STB and DTH, Study of FAX machine,group-3 fax machine, Fuzzy logic washing machine, study of digital camera, RFID & Bluetooth technology, study of iPods,MP4 players & accessories, block diagram of microwave oven and its function of each block.

Text Books:

Scheme and Syllabi for B. Tech-ICE, 1st year (Common to all branches) w.e.f batch 2014-15 and (2nd, 3rd & 4th years) w.e.f batch 2013-14 approved in the 22nd BOS of USSET on 30th June, 2014 and approved in the 37th AC Sub Committee Meeting held on 10th July, 2014.
Reference Books:
MOBILE COMMUNICATION

Paper Code: ETIC-418
Paper: Mobile communication

INSTRUCTION TO PAPER SETTER: MAXIMUM MARKS: 75
1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.
2. Apart from Q. No. 1 rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks.

Objective: The objective of the paper is to introduce the student about cellular concept, mobile radio propagation, fundamentals of equalizer, GSM and CDMA technique.

UNIT I:
The Cellular Concept-System Design Fundamentals
Mobile Radio Propagation: Large-Scale Path Loss

UNIT–II
Mobile Radio Propagation: Small –Scale Fading and Multipath
Small Scale Multipath propagation: Factors Influencing Small Scale Fading, Doppler shift. Impulse Response Model of a Multipath Channel: Relationship Between Bandwidth and Received power, Small-Scale Multipath Measurements: Direct RF Pulse System, Spread Spectrum Sliding Correlator Channel Sounding, Frequency Domain Channels Sounding.

UNIT–III
Parameters of Mobile Multipath Channels: Time Dispersion Parameters, Coherence Bandwidth, Doppler Spread and Coherence Time. Types of Small-Scale Fading: Fading effects Due to Multipath Time Delay Spread-Flat Fading, Frequency Selective Fading. Fading effects Due to Doppler Spread: Fast Fading, Slow Fading.
Equalization and Diversity:

UNIT -IV
Multiple Access Technique in Wireless Communications
Frequency Division Multiple Access, Time Division Multiple Access, Spread Spectrum Multiple Access, Space Division Multiple Access.
GSM: GSM Services and features, GSM System Architecture, GSM Radio Sub-system and GSM Channel Types.
Introduction to EDGE, HSPA, WCDMA, LTE etc.

[T1, T2] [No. of Hours: 11]

[T1, T2] [No. of Hours: 12]

[T1, T2] [No. of Hours: 10]
Text books:

Reference books:

Scheme and Syllabi for B. Tech-ICE, 1st year (Common to all branches) w.e.f batch 2014-15 and (2nd, 3rd & 4th years) w.e.f batch 2013-14 approved in the 22nd BOS of USET on 30th June, 2014 and approved in the 37th AC Sub Committee Meeting held on 10th July, 2014.
### ADHOC AND SENSOR NETWORKS

**Paper Code:** ETEC-406  
**Paper:** Ad Hoc and Sensor Networks  
**L T/P C**  
3 0 3

---

**INSTRUCTIONS TO PAPER SETTER:**  
**MAXIMUM MARKS: 75**

1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.
2. Apart from Q. No. 1 rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks.

---

**Objective:** The prerequisites are data communication networks, wireless communication and networks. The objective of the paper is to introduce infrastructure less wireless networking.

---

**UNIT I**  
**Ad Hoc Wireless Networks:**  
**MAC Protocols for Ad Hoc Wireless Networks:**  

---

**UNIT II**  
**Routing Protocols for Ad Hoc Wireless Networks:**  
**Transport Layer and Security Protocols for Ad Hoc Wireless Networks:**  

---

**UNIT III**  
**Wireless Sensor Networks:**  
**Hybrid wireless Networks:**  

---

**UNIT IV**  
**Wireless Geolocation Systems:**  
**Recent Advances in Wireless Networks:**  

---

**INSTRUCTIONS TO PAPER SETTER:**  
**MAXIMUM MARKS: 75**

1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.
2. Apart from Q. No. 1 rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks.

---

**Objective:** The prerequisites are data communication networks, wireless communication and networks. The objective of the paper is to introduce infrastructure less wireless networking.
Text Books:

Reference Books
ROBOTICS

Paper Code: ETMT-402
Paper: Robotics

<table>
<thead>
<tr>
<th>L</th>
<th>T/P</th>
<th>C</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>3</td>
<td>0</td>
<td>3</td>
<td></td>
</tr>
</tbody>
</table>

**INSTRUCTIONS TO PAPER SETTERS:**

1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.
2. Apart from question no. 1, rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks.

**MAXIMUM MARKS: 75**

Objective: To introduce the foundations of robotics. Also, a course on Robotics must use one or more software to not only visualize the motion and characteristics of robots but also to analyze/synthesize/design robots for a given application.

**UNIT - I**
Fundamentals of Robot Technology:
Robot definition, automation and robotics, Robot anatomy, Work volume, Drive systems. Control systems and dynamic performance. Accuracy and repeatability. Sensors and actuators used in robotics; Machine Vision; Robot configurations, Path control. Introduction to robot languages. Applications; Types (Mobile, Parallel); Serial: Cartesian, Cylindrical, etc.; Social Issues

**UNIT - II**
Robot Kinematics: Mapping, Homogeneous transformations, Rotation matrix, Forward Kinematics (DH Notation) and inverse kinematics: Closed form solution.

Robot Differential Motion: Linear and Angular velocity of rigid link, Velocity along link, Manipulator jacobian, Statics: Use of jacobian.

**UNIT – III**
Robot Dynamics: Lagrangian Mechanics, Lagrangian Formulation and numericals. Dynamics, Newton-Euler Recursive Algorithm, Simulation. Euler-Lagrange Equations of motion/Any other formulation like using Decoupled Natural Orthogonal Complements (DeNOC)

End effectors: Mechanical and other types of grippers. Tools as end effectors, Robot and effector interface. Gripper selection and design.

**UNIT - IV**

**Text Books:**


**Reference Books:**

OBJECT ORIENTED SOFTWARE ENGINEERING

Paper Code: ETCS-412  
L  T/P  C  
3    0    3

Paper: Object Oriented Software Engineering

INSTRUCTIONS TO PAPER SETTERS:

MAXIMUM MARKS: 75

1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.
2. Apart from question no. 1, rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks.

Objective: To make the students well versed with current s/w developments in the industry.

UNIT I:
Introduction – Overview of Object-Orientation; Basic Concepts of Object-Orientation; Data abstraction, Encapsulation, Inheritance, Aggregation, classes, objects, messages, inheritance, polymorphism. Importance of modeling, principles of modeling, Object oriented modeling.

OO Life cycle – Object Oriented analysis, modeling and design; Requirement Elicitation. Introduction to Object Oriented Methodologies, Overview of Requirements Elicitation, Requirements Model-Action & Use cases.

UNIT II:
Architecture: Introduction, System development is model building, model architecture, requirement model, analysis model, design model, implementation model.
Analysis: Introduction, System development based on user requirement, Use case model, interface descriptions, Problem domain objects, interface objects, entity objects, control objects.
Code Design Improvement: Refactoring, Anti patterns, Visitor Patterns.

UNIT III:
Construction: Introduction, the design model, design model dimensions, block design, working with construction.
Testing: Introduction, Object Oriented testing process, testing of analysis and design model, testing of classes.

UNIT IV:
Modelling with UML: Basic Building Blocks of UML; A Conceptual Model of UML.
Basic structural modelling: Classes, interfaces, Dependency, generalization and association relationship, comparison of E-R diagram and UML class Diagram, forward and reverse engineering.
Basic Behavioral Modeling: Use case diagram-relationships between use cases- extend, include, and generalize. Activity diagram-Action state, Activity state, Transition (Fork, Merge, Join), State diagram-events, State Diagram states, transitions, Interaction diagrams: Sequence diagram, Collaboration diagram (iterations, conditional messaging, branching, object creation and destruction, time constraints, origin of links.)
Architectural modelling:
Deployment: Common Modelling technique; Modelling processors and devices, modelling distribution of artifacts.
Collaboration: Modeling roles, modelling the realization of a Use Case, modelling the realization of an operation, modelling a mechanism.

Text Books:

Reference Books:
[R2] Booch, Maksimchuk, Engle, Young, Conallen and Houstan; “Object Oriented Analysis and Design with Applications”, Pearson Education.

INSTRUCTIONS TO PAPER SETTERS: MAXIMUM MARKS: 75

1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.
2. Apart from question no. 1, rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks.

Objective: To make the students well versed with current s/w developments in the industry.

UNIT I:
Introduction – Overview of Object-Orientation; Basic Concepts of Object-Orientation; Data abstraction, Encapsulation, Inheritance, Aggregation, classes, objects, messages, inheritance, polymorphism. Importance of modeling, principles of modeling, Object oriented modeling.

OO Life cycle – Object Oriented analysis, modeling and design; Requirement Elicitation. Introduction to Object Oriented Methodologies, Overview of Requirements Elicitation, Requirements Model-Action & Use cases.

UNIT II:
Architecture: Introduction, System development is model building, model architecture, requirement model, analysis model, design model, implementation model.
Analysis: Introduction, System development based on user requirement, Use case model, interface descriptions, Problem domain objects, interface objects, entity objects, control objects.
Code Design Improvement: Refactoring, Anti patterns, Visitor Patterns.

UNIT III:
Construction: Introduction, the design model, design model dimensions, block design, working with construction.
Testing: Introduction, Object Oriented testing process, testing of analysis and design model, testing of classes.

UNIT IV:
Modelling with UML: Basic Building Blocks of UML; A Conceptual Model of UML.
Basic structural modelling: Classes, interfaces, Dependency, generalization and association relationship, comparison of E-R diagram and UML class Diagram, forward and reverse engineering.
Basic Behavioral Modeling: Use case diagram-relationships between use cases- extend, include, and generalize. Activity diagram-Action state, Activity state, Transition (Fork, Merge, Join), State diagram-events, State Diagram states, transitions, Interaction diagrams: Sequence diagram, Collaboration diagram (iterations, conditional messaging, branching, object creation and destruction, time constraints, origin of links.)
Architectural modelling:
Deployment: Common Modelling technique; Modelling processors and devices, modelling distribution of artifacts.
Collaboration: Modeling roles, modelling the realization of a Use Case, modelling the realization of an operation, modelling a mechanism.

Text Books:

Reference Books:
[R2] Booch, Maksimchuk, Engle, Young, Conallen and Houstan; “Object Oriented Analysis and Design with Applications”, Pearson Education.
OBJECTIVE: The objective of this paper is to learn about the computer graphics and multimedia.

UNIT-I

UNIT-II
Clipping Algorithms, Sutherland-Cohen line Clipping Algorithm, Bezier Curves, B-Spline Curves. Parallel Projection, Perspective Projection, Illumination Model for diffused Reflection, Ambient light, Specular Reflection Model, Reflection Vector.

UNIT-III

UNIT-IV
Data Compression: storage space, coding requirements, Basic compression techniques: run length code, Huffman code, Lempel-Ziv JPEG: Image preparation, Lossy sequential DCT, expanded lossy DCT, Lossless mode, Hierarchical mode. MPEG, Media synchronization, Media Integration, Production Standards.

TEXT BOOKS:

REFERENCE BOOKS:
**ELECTROMAGNETIC FIELD THEORY**

**Paper Code:** ETIC-430  
**Paper:** Electromagnetic Field Theory

### INSTRUCTIONS TO PAPER SETTERS:

**MAXIMUM MARKS:** 75

1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.

2. Apart from Question No. 1, rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks.

**Objectives:** To list Maxwell’s equations and solve them for specific regular geometries, understand general electromagnetic wave propagation and how the plane wave solution can be used to approximate real situations, describe the boundary conditions for electric and magnetic fields at dielectric interfaces, interpret the effects of lossy and low loss dielectrics upon the propagation of electromagnetic waves, and predict this process in specific applications and solve the performance of specific transmission lines.

**UNIT I**

**Introduction:** Review of scalar and vector field, Dot and Cross products, Coordinate Systems-Cartesian, cylindrical and spherical. Vector representation of surface, Physical interpretation of gradient divergence and curl, Transformation of vectors in different co-ordinate systems, dirac-delta function.

**Electrostatics:** Electric field due to point-charges, line charges and surface charges, Electrostatic potential, Solution of Laplace and Poisson’s equation in one dimension, M-method of image applied to plain boundaries, field mapping and conformal transformation, Electric flux density, Boundary conditions. Capacitance: calculation of capacitance for simple rectangular, cylindrical and spherical geometries, Electrostatic energy.

**UNIT II**


**UNIT III**

**Electromagnetic Waves:** Continuity equations, Displacement current, Maxwell’s equation, Boundary conditions, Plane wave equation and its solution in conducting and non-conducting media, Phasor notation, Phase velocity, Group velocity, Depth of penetration, Conductors and dielectrics, Impedance of conducting medium. Polarization, Reflection and refraction of plane waves at plane boundaries, Poynting vectors, and Poynting theorem.

**UNIT IV**

**Transmission Lines:** Transmission line equations, Characteristic impedance, Distortion-less lines, Input impedance of a loss less line, computation of primary and secondary constants, Open and Short circuited lines, Standing wave and reflection losses, Impedance matching, Loading of lines, Input impedance of transmission lines, RF lines, Relation between reflection coefficient and voltage standing wave ratio (VSWR), Lines of different lengths – $\lambda/2$, $\lambda/4$, $\lambda/8$ lines, Losses in transmission lines, Smith chart and applications, impedance matching Single stub, Double stub.

**Text Books:**


**Reference Books:**

[R2] J.D. Kraus, “Electromagnetics”, TMH  

---

Scheme and Syllabi for B. Tech-ICE, 1st year (Common to all branches) w.e.f batch 2014-15 and (2nd, 3rd & 4th years) w.e.f batch 2013-14 approved in the 22nd BOS of USET on 30th June, 2014 and approved in the 37th AC Sub Committee Meeting held on 10th July, 2014.
SATELLITE COMMUNICATION

Paper Code: ETEC-404
Paper: Satellite Communication

<table>
<thead>
<tr>
<th>L</th>
<th>T/P</th>
<th>C</th>
</tr>
</thead>
<tbody>
<tr>
<td>3</td>
<td>0</td>
<td>3</td>
</tr>
</tbody>
</table>

**INSTRUCTIONS TO PAPER SETTERS:**

1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.
2. Apart from Question No. 1, rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks.

**MAXIMUM MARKS: 75**

Objectives: To study the most relevant aspects of satellite communication with emphasis on the most recent application & developments. It covers orbital mechanics, launching techniques, satellite link design, earth & space segment, error control coding and different multiple access techniques.

**UNIT- I**


**UNIT- II**

Satellite Link Design

Basic transmission, System noise temperature, G/T ratio, design of down links, uplink design, design of specified C/N, Atmospheric Absorption, Rain induced attenuation.

Space Segment: Power Supply, Altitude Control, Station Keeping, Thermal Control, TT&C sub system, Transponders, Antenna Sub system.

Earth Segment: Subsystem of earth station, Transmit-Receive Earth Station, different types of earth stations, frequency coordination.

**UNIT- III**

Multiple Access Techniques: FDMA, FDMA down link analysis, TDMA, Satellite-switched TDMA, code division multiple access, DAMA, On board signal processing for FDMA/TDM Operation.

Error Control for Digital Satellite Links: Error detection and correction for digital satellite links, error control coding, Convolutional codes, satellite links concatenated coding and interleaving, Automatic Repeat Request (ARQ).

**UNIT- IV**


Satellite Applications: Satellite mobile services, VSAT, GPS, Radarsat, INMARSAT, Satellite navigational system. Direct broadcast satellites (DBS)- Direct to home Broadcast (DTH), Worldspace services, Business TV(BTV)

**Text Books:**


**Reference Books:**


INTELLIGENT SYSTEMS & CONTROL LAB

Paper Code: ETIC–452                           L    T/P    C
Paper: Intelligent Systems & Control Lab      0    2     1

List of Experiments:

1. To generate triangular, trapezoidal and sigmoidal membership function using MATLAB Tool-box.
2. To write MATLAB program to generate sum, difference and product of the above membership function.
3. To compare the response of conventional PI Controller with its Fuzzy Counterpart using MATLAB Simulink.
4. To implement Fuzzy Controller for temperature control system using MATLAB Software.
5. To implement Mamdani type of Fuzzy Controller and to study the control output for different input combination.
6. To implement sugeno type of Fuzzy Controller for different input combination.
7. To implement Fuzzy Controller for 2nd/3rd order system using MATLAB.
8. Implementing Fuzzy Controller to control process variable such as temperature, pressure, flow etc.

NOTE:- At least 8 Experiments out of the list must be done in the semester.
VIRTUAL INSTRUMENTATION LAB

Paper Code: ETIC–454
Paper: Virtual Instrumentation Lab

List of Experiments:

Basic operations, Simple Programming Structures.

1. Lab VIEW - Debugging a VI, Sub VI’s
2. Lab VIEW ± Traffic Light ± Programming structure, Arrays, Clusters
3. Waveform measurements
4. Voltage to frequency Converter
5. Strain and Temperature with NI ELVIS
6. Signal Generation using NI- DAQmx, Frequency analysis
7. Oscilloscope ± Attribute Nodes, Menus
8. RC Circuit measurement ± Timing issues
9. Digital control of Stepper Motor
10. OPAMP circuits and its Characteristics
11. Digital to Analog Acquisition interfacing

NOTE:- At least 8 Experiments out of the list must be done in the semester.
EMBEDDED SYSTEMS LAB

Paper Code: ETIC-458 (ELECTIVE)  
Paper: Embedded Systems Lab

<table>
<thead>
<tr>
<th></th>
<th>L</th>
<th>T/P</th>
<th>C</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>0</td>
<td>2</td>
<td>1</td>
</tr>
</tbody>
</table>

List of Experiments:

1. Introduction to microcontroller and interfacing modules.
2. To interface the seven segment display with microcontroller 8051
3. To create a series of moving lights using PIC on LEDs.
4. To interface the stepper motor with microcontroller.
5. To display character ‘A’ on 8*8 LED Matrix
6. Write an ALP to add 16 bits using ARM 7 Processor
7. Write an ALP for multiplying two 32 bit numbers using ARM Processor
8. Write an ALP to multiply two matrices using ARM processor

NOTE:- At least 8 Experiments out of the list must be done in the semester
CONSUMER ELECTRONICS LAB

Paper Code: ETIC-458(ELECTIVE)       L  T/P  C
Paper: Consumer Electronics Lab        0   2   1

List of Experiments:

1. To plot the frequency response of a microphone
2. To plot the frequency response of a loud speaker
3. Demonstration of a tape-transport mechanism
4. Trouble shooting of tape-recorder system
5. To observe the wave forms and voltage B/W and colour T.V receiver.
7. Trouble shooting of C.D. Player
8. Demonstration of DVD Player.
10. Demonstration of the working of automatic, semi automatic washing machine & microwave oven.

NOTE:- At least 8 Experiments out of the list must be done in the semester.
**ROBOTICS LAB**

**Paper Code:** ETIC-452 (ELECTIVE)  
**Paper:** Robotics Lab  
**L T/P C**  
0 2 1

### List of Experiments:

1. Study of robotic arm, end effectors and its configuration and introduction to any software (such as workspace) used to simulate or program a robot;  
2. Program / simulate a robot for moving on a path;  
3. Program / simulate a robot for pick and place operation;  
4. Program / simulate a robot for welding operation;  
5. Program / simulate a robot for water jet machining;  
6. Program / simulate a robot for saving it from striking any other object in workspace;  
7. Program / simulate two robots working together;  
8. Make a 3R robot and simulate its motion.  
9. Use a microcontroller to program simple toy robot / model robot;  
10. Micro controller program to use different sensors and further move toy robot(s) / model robot;  
11. Use MATLAB / Scilab. Any other software to program numericals (Robot Arm kinematics) taught in class.  
12. Use MATLAB / Scilab and other robot specific software like Robo-Analizer for the study of kinematic and dynamics of 3R robots.  
13. Demos of a real robot; Introduce Virtual Robotics Lab. in ADAMS or SimMechanics of MATLAB.

**Note:**

a. Total Experiments are to be 12 (Twelve).

b. Experiments suggested by committee are given above- Choose any eight.

c. Rest (In above list / not in list) is liberty of respective institute to choose as per syllabus.

d. Suggested Software

A course on Robotics must use one or more software to not only visualize the motion and characteristics of robots but also to analyzer/synthesize/design robots for a given application (say, as class projects). Typical software which can be used are as follows:

- RoboAnalyzer (Developed by IIT Delhi; http://www.roboanalyzer.com)
- Virtual Labs. (Developed by IIT Kharagpur; http://vlabs.iitkgp.ernet.in/moodle/)
- MATLAB, its modules Simulink and SimMechanics (http://www.mathworks.com)
- Mathematica: Symbolic software (http://www.wolfram.com)
- Multi Bondgraph (http://bondgraph.org)
- ADAMS (by MSC software; http://www.mscsoftware.com)
- RerurDyn (by Function Bay, Korea; http://www.functionbay.co.kr)

**e. Other Aids**

- **Possible Class projects and presentations:** Kinematic/Dynamic modeling, programming, and analyses of a robotic arm (say, an RP manipulator); 2. Modeling of an AGV; 3. Building prototypes using, say, LEGO kits
- Video of practical applications
- Industry visits
- Robocon competitions: A national-level competition held every year during the 1st weekend of March

**NOTE:-** At least 8 Experiments out of the list must be done in the semester.
OBJECT ORIENTED SOFTWARE ENGINEERING LAB

Paper Code: ETIC-412 (ELECTIVE)  L   T/P   C
Paper: Object Oriented Software Engineering Lab  0   2   1

List of Experiments:

1. Design a case study for a real world scenario with problem statement and requirement specification.
2. Design a use case diagram for the problem statement identified.
3. Prepare an object and class diagram with all the relationships, cardinality, attribute & operations.
4. Depict the classes formed for the problem statement as Interaction Diagram (Prepare proper sequential timeline pattern for all the use cases thus derived).
5. Depict the classes so formed as activity diagram.
6. Prepare a state-chart diagram for the problem statement thus formed.
7. Employ common modeling techniques for the component and deployment diagram.

NOTE:- At least 8 Experiments out of the list must be done in the semester.