



# GURU GOBIND SINGH INDRAPRASTHA UNIVERSITY

Sector 16-C, Dwarka, New Delhi – 110078

## EXAMINATION DIVISION

### **Conduct Branch-I**

PROPOSED PRACTICAL DATE SHEET FOR END TERM EXAMINATIONS (APRIL 2017)

Programme: M. Tech. (CSE, IT and VLSI Design) – Regular & Reappear

Only for C-DAC Students

Timing 10:00 AM Onwards

Date/Day	Course	Course Code	Subject
24.04.2017 Monday	CSE	MECS-652	Object Oriented Analysis & Design Lab
	IT	MEIT-652	Advanced Mobile Computing Lab
	VLSI Design	MEVS-652	ESD Lab
26.04.2017 Wednesday	CSE	MECS-654	Advanced Data Base Management System Lab
	IT	MEIT-654	Advanced Software Project Management Lab
	VLSI Design	MEVS-654	Analog VLSI Design Lab
28.04.2017 Friday	CSE	MECS-656	Advanced Algorithm Analysis & Design Lab
	IT	MEIT-656	Computer Graphics & Animation Lab
	VLSI Design	MEVS-656	Low Power VLSI Design Lab

Note: Any representation regarding the proposed dates/ paper etc. may be forwarded to the Controller of Examination/Officer Incharge (Conduct) through the Coordinator-cum-Convener of M. Tech. (CSE, IT and VLSI Design) program latest by 3<sup>rd</sup> April, 2017.

For any clarification and query please contact:

Section Officer (Conduct-I)	011-25302263
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(A.D. Lamba)  
Dy. Registrar (Examinations)

(Prof. Pravin Chandra)  
Controller of Examinations(Operations)

Dated : 21.03.2017

