

F.No. 51(1)(1)/2026/USIC&T/ _____

Dated: 16th October, 2026

NOTICE

Interview for the selection of Post-Doctoral Fellow (PDF) under the Visvesvaraya Ph.D. Scheme

The interview for the selection of one (01) Post-Doctoral Fellow (PDF) under the Visvesvaraya Ph.D. Scheme for Electronics & IT: Phase-II shall be held as per the following schedule:

Date - 23rd October, 2026 (Friday)

Time - 10:00 a.m.

Venue - ECR-315, E-Block, USIC&T, GGSIPU, Dwarka, Sector 16C, New Delhi - 110078

The following eligible applicants are required to appear for an interview and present their research proposals before the Admission Committee constituted for the selection of the Post-Doctoral Fellow (PDF):

S. No.	Name of Applicant	Nationality	Date of Birth	Category	Year of Award of Ph.D. Degree
1.	Anurag Tiwari	Indian	27.11.1989	UR	2025
2.	Asha	Indian	25.02.1989	UR	2023
3.	Manik	Indian	21.05.1992	UR	2025
4.	Neha Pannu	Indian	20.01.1992	UR	2023
5.	Parikha Chawla	Indian	13.08.1988	UR	2024
6.	Sujit Tripathy	Indian	11.07.1995	UR	2026
7.	T. Kesavan	Indian	16.06.1987	UR	2024
8.	Yash Verma	Indian	01.05.1991	UR	2024

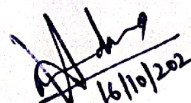
The candidates shall be permitted to appear for the PDF interview, subject to verification of their documents.

Instructions:

Candidates are required to bring the following documents on the day of the interview:

1. Uploaded Application form in original.
2. Two (02) hard copies of Research proposal.
3. A Power point presentation (PPT) of the Research Proposal in pen drive.
4. All original certificates, degrees, and other documents as mentioned in the submitted application form.
5. The conversion formula for the grading system of the awarding university, wherever applicable.
6. Original category certificate, wherever applicable, for SC/ST/PwD/EWS/OBC(Non-Creamy Layer) candidates.
7. A copy of a brief résumé.
8. Copies of all research publications and patents, wherever applicable.
9. Two (02) recent passport-size photographs.

Note: All candidates must report as per the schedule. No TA/DA shall be paid for the interview. Non-appearance in interview shall lead to cancellation of candidature.


(Dr. Vandana Nath)
Professor, USIC&T
Nodal Officer, GGSIPU
Visvesvaraya Ph.D. Scheme for Electronics & IT: Phase II

Copy to:

1. Dean, USIC&T, for information please.
2. All SRC members, for information please.
3. Director (RDC), for information please.
4. In-charge, UITS, with request to upload on the university website notices.
5. Guard file.